320-to-40-Gb/s Demultiplexing Using a Single SOA Assisted by an Optical Filter

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Abstract—We demonstrate excellent all-optical demultiplexing of 40-Gb/s base-rate channels out of 160- and 320-Gb/s single polarization optical time-division-multiplexed data streams. The demultiplexer utilizes a semiconductor optical amplifier and an optical filter placed at the amplifier output. The center wavelength of the filter is blue-shifted from the wavelength of the clock signal, so that ultrafast chirp dynamics can be employed for optical switching. Error-free demultiplexing was achieved at very low optical switch powers: 3.5 mW (160-Gb/s data), 6.3 mW (320-Gb/s data), and 0.09 mW (40-GHz clock). The proposed demultiplexer has a simple structure and allows monolithic integration.

Index Terms—Optical time-division multiplexed (OTDM) demultiplexing, semiconductor nonlinear optics, semiconductor optical amplifier (SOA).

I. INTRODUCTION

OPTICAL demultiplexers, which extract a signal channel at the base rate of a time-multiplexed high-bit-rate data stream, form a key functionality in optical time-division-multiplexed (OTDM) networks. Optical demultiplexers with ultrashort switching gates can be realized by utilizing fiber nonlinearities [1] and semiconductor devices [2]–[4]. Fiber-based optical switches can provide an ultrabroad conversion band and ultra-high-speed operation but at the expense of high optical powers and performance instability. Semiconductor optical amplifier (SOA)-based demultiplexing techniques are particularly attractive because they feature small size, high stability, low switching energy, and high integration potential.

A number of SOA-based demultiplexers have been proposed in the literature. Four-wave mixing in an SOA can successfully demultiplex 160–40 Gb/s [2], but this scheme suffers from low efficiency, preventing it to operate for higher data rates. All-optical switches using SOAs in an optical interferometer can realize 160–10 Gb/s [3], however, the maximum attainable base rates are severely limited by the SOA slow recovery time. Electro-optical switches have been employed to demultiplex 40-Gb/s base channels from a 160-Gb/s data stream [4], but a shorter switching time is difficult to achieve.

In this letter, we present a high-performance all-optical demultiplexer employing a single SOA. We show that the concept is able to demultiplex 160/320 to 40 Gb/s with very low switching powers. The SOA has an initial gain recovery time of >90 ps, which based on its length of 800 μm can only demultiplex error-free OTDM signals to base rates of <40 Gb/s using conventional concepts. An essential part in our approach is that we employ an optical bandpass filter which is slightly detuned from the data signal (see [5] and references therein) to utilize ultrafast chirp dynamics in the SOA [6], thus shortening the recovery time of the demultiplexer. This fast recovery results in higher attainable base rates, i.e., 40 Gb/s and above. A similar approach has been successfully demonstrated for 160-Gb/s wavelength conversion [6]. This approach has a simple configuration and allows photonic integration.

II. EXPERIMENTAL SETUP AND RESULTS

The experimental setup as shown in Fig. 1 consists of an OTDM transmitter and an OTDM receiver in a back-to-back configuration. The setup was constructed with commercially available components. For the transmitter, a 2-ps 10-GHz optical clock signal (λ₀ = 1545 nm) that outputs from an actively mode-locked fiber laser (MLFL-1) is optically quadrupled (×4) to 40 GHz and is subsequently modulated to form a 40-Gb/s base rate of 2^7–1 return-to-zero pseudorandom binary sequence (PRBS). This 40-Gb/s data signal is further time-multiplexed to 160 or 320 Gb/s by using passive pulse interleavers (×2; ×4).
For the 320-Gb/s OTDM transmitter, the 40-GHz clock pulses are compressed down to 0.8 ps by using a 1-km dispersion-decreasing fiber (DDF). The 160/320-Gb/s data signal is combined with a clock signal that consists of a 40-GHz 1.8-ps pulse train. This signal is at $\lambda_c = 1560$ nm and is also generated by optically quadrupling ($\times 4$) the 10-GHz pulses that output from MLFL-2. The data and control pulses are coupled into the OTDM receiver via a 3-dB coupler. The receiver comprises the proposed demultiplexer and a 40-Gb/s photodetector. As shown in Fig. 1(a), our demultiplexer is made out of an SOA and a 1.5-nm optical filter (BPF3) for creating an ultrashort switching gate. To improve the optical signal-to-noise ratio (OSNR) of the demultiplexed signal, we used an erbium-doped fiber amplifier (EDFA) and a 0.5-nm optical filter (BPF4). The SOA is a product from Kamelian that was designed as a preamplifier. The 800-μm SOA has the following properties: the bias current is 250 mA, the small signal gain 23 dB, the saturation output power 12 dBm, and the noise figure 6.5 dB. The SOA linewidth enhancement factor is estimated to be 3–7 [6]. The average optical powers injected to the SOA for optimum demultiplexing are 3.5 mW (160-Gb/s data signal), 6.3 mW (320-Gb/s data signal), and 0.09 mW (40-GHz control signal).

The working principle of this concept has been expeditiously explained in [5] and [6] for the case of wavelength conversion. The continuous wave probe light in [5] and [6] is replaced by the 40-GHz control pulses in our demultiplexer. The principle can be summarized as follows. In the SOA, the injected data signal at $\lambda_c$ acts as pump and modulates the SOA gain. As a result, the control pulse at $\lambda_c$ is affected via cross-gain modulation, causing an inverted demultiplexing operation. Moreover, the injected data signal also modulates the refractive index of the SOA, resulting in a chirped control signal. The leading edge of the control pulse is red-shifted, whereas the trailing edge is blue-shifted. As a consequence, the spectrum of the control pulse is broadened. The center passband of BPF3 at the SOA output is slightly blue-shifted from the control wavelength in order to compensate the slow gain recovery, leading to a narrow and inverted switching window with steep slopes. The spectral location of BPF3 relative to the data and control pulse is depicted in Fig. 2 for the case of demultiplexing 160 Gb/s. The BPF3 slope at $\lambda_c$ is approximately $-0.002/\text{GHz}$. It can be observed in Fig. 2 that the central wavelength of BPF3 is located approximately 1 nm from the central wavelength of the control pulses. This setting introduces approximately 10-dB additional loss in the control signal. Therefore, an EDFA is placed at the output of BPF3 to enhance the signal OSNR above the desired detection threshold.

Fig. 1(b) and (e) shows the eye diagram of 160 and 320 Gb/s, respectively, as observed by using an optical sampling oscilloscope with an optical measurement bandwidth up to 700 GHz. Fig. 1(c) and (f) depicts the demultiplexed 40 Gb/s as taken directly after BPF3 for 160- and 320-Gb/s input signal, respectively. For the 160-40 Gb/s demultiplexing, the eyes are clearly open with a residual pulse fragment around the zero level. This eye performance is much better than the eyes of the 320-40 Gb/s demultiplexing. This residual pulse is caused by the fact that the control pulse [1.8-ps full-width at half-maximum (FWHM)] is much wider than the data pulse (0.8-ps FWHM). Since the data pulse for 320 Gb/s is shortened by the long DDF, the data pulse drifts continuously from the control pulse, making difficult to perform an accurate eye-diagram analysis. This slow drift is largely caused by the small variation in the ambient thermal condition of the DDF. The clear opening of the eye diagrams in Fig. 1(c) and (f) demonstrates a significant improvement of the recovery time to less than 3 ps. To increase the signal contrast ratio, we employed an EDFA followed by a narrow filter BPF4, i.e., stretching the pulse within the bit period. The output of the 0.5-nm BPF4 is displayed in Fig. 1(d) and (g). The pulse becomes wider but the eye opening improves considerably, relaxing the setting of the detection threshold. The base-rate signal is then coupled into the 40-Gb/s detector and a bit-error-rate (BER) tester. The tester used the clock signal of the PRBS generator. Fig. 3(a) shows BER values of the 40-Gb/s base-rate signal from 160 and 320 Gb/s. For comparison purposes, we also present in Fig. 3(a) BER values of the 40-Gb/s basic channel before the demultiplexing stage. First, we detail the case of demultiplexing 160–40 Gb/s. It can be observed from Fig. 3(a) and (b) that all four 40-Gb/s tributaries of 160 Gb/s...
show an identical performance and that the penalty for BER $= 10^{-9}$ is less than 3.5 dB when compared to the reference 40-Gb/s basic channel. This penalty can be considered small since it also includes penalties of the multiplexing stage. Moreover, it is visible that no error floor is observed, which indicates excellent performance of the demultiplexer. Next, a pulse compressor is employed to achieve shorter pulses for multiplexing to 320 Gb/s. A demultiplexed 40-Gb/s eye diagram is presented in Fig. 3(c). Fig. 3(a) shows that demultiplexing 320–40 Gb/s using this concept gives varying results. The eight 40-Gb/s tributaries show error floors, but all below a BER of $10^{-9}$. The BER values are significantly better than the forward-error-correction threshold for BER $= 10^{-12}$. We believe that the error floors are caused by the wide control pulse, the drift of the data pulse, and the high noise level of the data pulse. The input OSNR of the demultiplexer on average is less than 17 dB, inducing less chirp in the SOA and, therefore, less contrast ratio of the demultiplexing gate than the case of 160 Gb/s. Moreover, the absence of synchronization between the data and the control pulses made it extremely difficult to keep the demultiplexing gate constant. Better signal contrast ratios are possible if the control pulse is compressed to the same width or even narrower than the data pulse. Chirp induced by the data pulse will then affect the entire width of the control pulse, and not only a part of it as in the present case.

Plots in Fig. 3 are obtained for constant data and control powers. This is in practice not always the case because the data signal arriving at receivers fluctuates in power due to various transmission impairments. Fig. 4 shows the penalties as a function of power deviations of the data signal at the input of the SOA. The plots in Fig. 4 were made as follows. The net effect of chirp depends on the total input power to the SOA for a certain pulsewidth. For each curve, we initially set the data signal power at the SOA input to correspond to BER of $10^{-9}$. Then, we varied the data signal power to worsen the receiver sensitivity, which is followed by tuning the power of the control pulse to return the system performance back to BER of $10^{-9}$. We performed this input dynamic range measurement for 160 and 320 Gb/s. For low input powers, the performance is asymptotically bounded by low OSNR values. For high input powers, the performance deteriorates due to the too strong gain saturation, causing wider demultiplexing gate. For a penalty of 3 dB as the maximum allowable deterioration, the demultiplexer can cope with the fluctuation of the data power as much as 6 dB for 160 Gb/s and 2 dB for 320 Gb/s. The small dynamic range of the 320-to-40-Gb/s demultiplexer is due to the low input OSNR and the instability of the demultiplexing gate.

III. DISCUSSION AND CONCLUSION

The SOA in the setup initially has $\geq 90$-ps recovery time. For demultiplexing based on conventional cross-gain/phase modulation, this 800-μm SOA only allows operating bit rates of less than 40 Gb/s. Our approach achieved $< 3$-ps recovery time, enabling us to process 320 Gb/s. This data rate is higher than the rates reported in [2]–[4]. The crucial point here is to utilize BPF3 to select the blue-shifted sideband of the chirped control pulses. The center of BPF3 is detuned by 1 nm to the blue side of the control wavelength. This detuning is a compromise between the maximum contrast ratio and the minimum power loss of the switching gate. In the setup, an additional EDFA was used to compensate for losses affected by the detuned filter. The use of an EDFA is not essential here since optical gain can be delivered by an SOA. The proposed demultiplexer does not restrict itself to a specific device. However, a better demux performance can be obtained if an SOA is used that produces more chirp.

In conclusion, we have demonstrated a new concept of time-domain demultiplexing that is based on the filtered chirp using only one SOA. The demultiplexer has been successfully tested for 160-to-40-Gb/s demultiplexing with small penalties. A reasonably good performance is also obtained for 320-to-40-Gb/s demultiplexing. Although all eight 40-Gb/s tributaries show relatively low error floors, a considerable improvement in BER values is believed to possibly be realized if the performance of the data, control pulse, and their synchronization are enhanced. Finally, we wish to underline that the proposed demultiplexing concept has a simple configuration, requires low switching powers, and allows photonic integration.

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REFERENCES


