CLASSIFICATION AND COMPOSITION OF DELAY-INSENSITIVE CIRCUITS

PROEFSCHRIFT

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you only grow by coming to the end of something and by beginning something new

from ‘The World according to Garp’
	by John Irving
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Introduction

VLSI technology appears to be a powerful medium to realize highly concurrent computations. The fact that we can now fabricate systems that are more complex and more parallel makes high demands, however, upon our ability to design reliable systems. Our main concern in this monograph is to address the problem of specifying components in such a way that, when a number of them is composed using a VLSI medium, the specification of the composite can be deduced from knowledge of the specifications of the components and of the way in which they are interconnected. We confine our attention to temporal and sequential aspects of components and do not, for example, discuss their layouts.

For the specification and composition of components we use a discrete and metric-free formalism, which can be used for the design of concurrent algorithms as well. Therefore, the separation of the design of concurrent algorithms from their implementation as chips, which we have actually introduced in the preceding paragraph, does not seem to move the two too far apart. In fact, we believe that this formalism constitutes a good approach to a mechanical translation of algorithms into chips.

A typical VLSI circuit consists of a large number of active electronic elements. It distinguishes itself from LSI circuits by a signiﬁcantly larger amount of transistors. Unfortunately, existing layouts for circuits cannot simply be mapped onto a smaller area as technology improves. The behaviour of a circuit may change when it is scaled down, since assumptions made for LSI are no longer valid for VLSI. The reason is that parameters determining a circuit’s behaviour do not scale in the same way, when the size of that circuit is scaled down.
As has been argued in [9], scaling down a circuit's size by dividing all dimensions by a factor α results in a transit time of the transistors that is a times shorter. The propagation time for an electrical signal between two points on a wire, however, is the same as the propagation time for an electrical signal between the two corresponding points in the scaled circuit. In VLSI circuits the relationship between delay and transit time becomes such that delays of signals in connecting wires might not be neglected anymore.

From the above we conclude that, if we want circuit design to be independent of the circuit's size, we have to employ a method that relies neither upon the speed with which a component or its environment responds nor upon the propagation delay of a signal along a connecting wire. The resulting kind of components we call delay-insensitive. Another advantage of delay-insensitive components is that we have a greater layout freedom, since the lengths of connecting wires are no longer relevant to correctness of operation.

Apart from the reasons mentioned above, there is yet another motive for the design of delay-insensitive circuits. In a lot of concurrent computations a so-called arbitration device is used. Basically, such a device grants one out of several requests. Real-time interrupts are a typical example of the use of such a device. In its simplest form it can be viewed as a bistable device. Consequently, under some continuity assumptions [4], it has a metastable state. The closer the initial state is to the metastable state the longer it takes before it settles down in one of its stable states. Starting from the metastable state it even may never end up in a stable state.

In clocked systems, where all computational units are assumed to complete each of their computations within a fixed and bounded amount of time, this so-called glitch phenomenon may lead to malfunctioning. This problem was first signalled in the late sixties [0,8]. The only way to guarantee fully correct communications with an arbitration device is to make the communicating parts delay-insensitive. This is not the way, however, in which this problem is solved in present-day computers, where the probability of correct communications is made sufficiently large, by allowing, for example, on the average one failure of this kind a year. This is achieved by reducing the clock rate and, hence, the computation speed. From an industrial point of view this may be quite satisfactory. From a theoretical point of view it certainly is not.

+++

In this monograph the foundation of a theory on delay-insensitive circuits is laid. The notion of delay-insensitivity is formally defined and a classification of delay-insensitive components is given in an axiomatic way. Moreover, a composition operator for these components is introduced and its correctness is discussed. Crucial to this discussion is that we do not want to assume anything about absolute or relative delays in wires that connect these components, except that delays
are non-negative. This leads to two conditions that should be complied with upon composition.

First, in order to prevent a voltage level transition from interfering with another one propagating along the same wire at most one transition is allowed to be on its way along a wire, since successive voltage level transitions may propagate at different speeds. At best, this kind of interference leads to absorption of transitions, which can be viewed as an infinite delay. At worst, however, it causes the introduction of new transitions, which may lead to malfunctioning. Therefore, absence of transmission interference is to be guaranteed upon composition.

Second, we have to guarantee absence of computation interference. Computation interference is the arrival of a voltage level transition at a circuit before that circuit is ready - according to its specification - to receive it. In other words, an input signal should not interfere with the computation that goes on before the circuit is ready for that signal's reception. Due to unknown wire delays, this amounts to not sending a signal before the receiver is ready for it.

How to get delay-insensitive circuits in the first place is not a topic addressed here. One can follow the method proposed by Seitz [11] and divide a chip into so-called isochronic regions. These regions are so small that, within a region, the wire delays are negligibly small. They are then interconnected by wires with delays about which no assumptions are made. The smaller the regions are chosen the less sensitive such circuits will be to scaling. Another method is the one proposed by Fang and Molnar [3]. They model a circuit as a Huffman asynchronous sequential circuit with certain of its inputs consisting of feedback values of some of its outputs. Then it can be shown that the circuit thus obtained is delay-insensitive in its communications with the environment, provided that both the combinational circuit and the internal delays meet certain conditions.

A communication protocol that is often used for databuses [13] allows a number of voltage level transitions to occur on a wire before the final level on that wire represents a signal and can be inspected. The presence of such a final level on a wire is then signalled by a so-called data valid signal, for which a second wire is used. For this kind of protocol, however, we need to know something about the relative wire delays, for which, for example, so-called bundling constraints can be used. As pointed out above, we do not want to assume anything about absolute or relative wire delays and, hence, we do not investigate this kind of protocol. The approach that is advocated here is first to understand the composition of fully delay-insensitive circuits and next to decide whether and how to incorporate items like bundling constraints. Consequently, we assume transitions from one voltage level to another to be monotonic.
In the first chapter we summarize trace theory and discuss a composition operator, blending, in particular. A more comprehensive discussion can be found in [12]. Trace theory is a discrete and metric-free formalism, in which we can adequately define notions such as delay-insensitivity and absence of computation and transmission interference. In the subsequent chapter we define and classify delay-insensitive components. This classification is illustrated by a number of examples. The third chapter is devoted to partitioning the wires of these components into independent groups via which composition is possible. In addition, we state a number of conditions that must be satisfied if this composition is to be allowed. In the subsequent chapter it is argued that, under these conditions, there is no computation and transmission interference. Moreover, it turns out that we can specify the composite by means of the blend of the specifications of the composing parts. The fifth chapter shows which of the classes introduced in Chapter 2 are closed under this composition operator. Finally, in Chapter 6, some clues are given to relax the composition conditions in order to incorporate other, more general, kinds of compositions for delay-insensitive circuits than the ones discussed here.

A slightly unconventional notation for variable-binding constructs is used. It will be explained here informally. Universal quantification is denoted by

$$\forall : D : E$$

where $\forall$ is the quantifier, $l$ is a list of bound variables, $D$ is a predicate, and $E$ is the quantified expression. Both $D$ and $E$ will, in general, contain variables from $l$. $D$ delineates the domain of the bound variables. Expression $E$ is defined for variable values that satisfy $D$. Existential quantification is denoted in a similar way with quantifier $\exists$. In the case of set formation we write

$$\{ l : D : E \}$$

to denote the set of all values of $E$ obtained by substituting for all variables in $l$ values that satisfy $D$. The domain $D$ is omitted when obvious from the context.

For expressions $E$ and $G$, an expression of the form $E \Rightarrow G$ will often be proved in a number of steps by the introduction of intermediate expressions. For instance, we can prove $E \Rightarrow G$ by proving $E = F$ and $F \Rightarrow G$ for some expression $F$. In order not to be forced to write down expressions like $F$ twice, expressions that often require a lot of paper, we record proofs like this as follows.

$$E$$

$$= \{ \text{hint why } E = F \}$$
\[ F \rightarrow (\text{hint why } F \rightarrow G) \]

\[ G \]

We shall frequently use the hint calculus, viz. when appealing to everyday mathematics, i.e. predicate calculus, arithmetics, and, above all, common sense. These notions have been adapted from [2].
1

Trace theory

In order to define and classify delay-insensitive circuits we need a formalism for their specification. For that purpose we use trace theory. In the present chapter we give an overview of trace theory as far as we need it for this monograph. A more thorough discussion can be found in [12].

1.0. Traces and trace structures

An alphabet is a finite set of symbols. Symbols are denoted by identifiers. For each alphabet $A$, $A^*$ denotes the set of all finite-length sequences of elements of $A$, including the empty sequence, which is denoted by $\varepsilon$. Finite-length sequences of symbols are called traces. A trace structure $T$ is a pair $\langle U, A \rangle$, in which $A$ is an alphabet and $U$ a set of traces satisfying $U \subseteq A^*$. $U$ is called the trace set of $T$ and $A$ is called the alphabet of $T$. The elements of $U$ are called traces of $T$ and the elements of $A$ are called symbols of $T$.

We postulate operators $t$, $a$, $i$, and $o$ on trace structures. For trace structure $T$, $t T$ and $a T$ are the trace set of $T$ and the alphabet of $T$ respectively. $i T$ and $o T$ are disjoint subsets of $a T$. $i T$ is called the input alphabet of $T$ and $o T$ the output alphabet. Notice that $i T \cup o T$ need not be equal to $a T$.

An informal mechanistic appreciation of a trace structure is the following. A trace structure is viewed as the specification of a mechanism communicating with its environment. Symbols of the trace structure’s alphabet are the various kinds of communication actions possible between mechanism and environment. The input symbols of the trace structure are inputs with respect to the mechanism and outputs with respect to the environment. The output symbols of the trace structure are outputs with respect to the mechanism and inputs with respect to the environment. A trace structure’s trace set is the set of all possible sequences of communication actions that can take place between the mechanism
and its environment.

With a mechanism in operation we associate a so-called trace thus far generated. This is a trace of the trace structure of that mechanism. Initially the trace thus far generated is \( \varepsilon \), which apparently belongs to the trace structure. Each act of communication corresponds to extending the trace thus far generated with the symbol associated with that act of communication.

This appreciation pertains to a mechanism more abstract than an electrical circuit. It enables us to explore in the next two chapters properties that may be associated with delay-insensitivity. In Chapter 4, finally, we are able to give a mechanistic appreciation of trace structures that is tailored to electrical circuits.

**Example 1.0**

A Wire is allowed to convey at most one voltage level transition. We assume that there are two voltage levels, viz. low and high. Hence, we can view a wire as a mechanism that is able to accept either a voltage level transition from low to high, whereafter it produces the same transition at its output, or to accept a voltage level transition from high to low, whereafter it produces that transition at its output again. Since the two kinds of transitions alternate, we do not make a distinction in our formalism between a high-going and a low-going transition. Consequently, the specification of such a wire is a trace structure with input alphabet \( \{ a \} \), output alphabet \( \{ b \} \), and trace set the set of all finite-length alternations of \( a \) and \( b \) that do not start with \( b \).

(End of Example)

**Note:** Unless stated otherwise, small and capital letters near the end of the Latin alphabet are used to denote traces and trace structures respectively. Small and capital letters near the beginning of the Latin alphabet denote symbols and alphabets respectively.

(End of Note)

The projection of trace \( t \) on alphabet \( A \), denoted by \( t \upharpoonright A \), is defined as follows

\[
\begin{align*}
\text{if } t = \varepsilon \text{ then } t \upharpoonright A &= \varepsilon \\
\text{if } t = u a \land a \in A \text{ then } t \upharpoonright A &= (u \upharpoonright A)a \\
\text{if } t = u a \land a \notin A \text{ then } t \upharpoonright A &= (u \upharpoonright A)
\end{align*}
\]

(concatenation is denoted by juxtaposition.)
The projection of a trace set $T$ on alphabet $A$, denoted by $T[A]$, is the trace set $\{ t : t \in T : t[A] \}$ and the projection of trace structure $T$ on $A$, denoted by $T[A]$, is the trace structure $< (t[A], aT \cap A) >$. The input alphabet $i(T[A])$ and the output alphabet $o(T[A])$ of $T[A]$ are defined as $iT \cap A$ and $oT \cap A$ respectively.

Property 1.0: Projection distributes over concatenation, i.e. for traces $t$ and $u$, and for alphabet $A$ $(tu)[A] = (t[A])u[A]$. 

Property 1.1: For trace $t$ and alphabets $A$ and $B$ $t[A]B = t[A \cap B]$. 

In order to save on parentheses we give unary operators the highest binding power, and write $tT[A]$ instead of $(tT)[A]$. Moreover, concatenation has a higher binding power than projection. As a consequence, we write $tu[A]$ instead of $(tu)[A]$. For trace $t$ the length of $t$ is denoted by $l(t)$. For trace $t$ and symbol $a \notin t$, $t(a)$ denotes the number of occurrences of $a$ in $t$. We call trace $a$ a prefix of trace $t$ if $(3u : au = t)$. For trace set $T$, the trace set that contains all prefixes of traces of $T$ is called the prefix-closure of $T$, and is denoted by $\text{pref}T$. A trace set $T$ is called prefix-closed if $T = \text{pref}T$.

Property 1.2: For prefix-closed trace set $T$ and alphabet $A$ $T[A]$ is prefix-closed.

There are two composition operators that we shall frequently use. The first one is weaving. It can, for the time being, be appreciated as the composition of two mechanisms where each communication in the intersection of the two alphabets is the same for both mechanisms. This leads to the following definition. The weave of two trace structures $S$ and $T$, denoted by $SwT$, is the trace structure

$< \{ x : x \in (aS \cup aT)^* \land x[\land x[\land x[aS \in tS \land x[aT \in tT : x] , aS \cup aT] >$

Input and output alphabet of $SwT$ are defined as $(iS \cup iT) \setminus (aS \cap aT)$ and $(oS \cup oT) \setminus (aS \cap aT)$ respectively. Apparently, the type of non-common symbols does not change and common symbols loose their types.
Example 1.1
\[ \langle \{ ab, a b e, d e \}, \{ a, b, d, e \} \rangle \times \langle \{ b e, b e c, f e \}, \{ b, e, e, f, f \} \rangle = \langle \{ ab, a b e, d e, f d e \}, \{ a, b, e, d, e, f \} \rangle \]

(End of Example)

Property 1.3: For trace structures \( S \) and \( T \), for traces \( s \) and \( t \), and for symbols \( a \in S \setminus a T \) and \( b \in a T \setminus a S \)

\[ s a b t \in t(S \cdot w T) \rightarrow s b a t \in t(S \cdot w T) \]

Proof:
\[ s a b t \in t(S \cdot w T) \]
\[ = \{ \text{definition of weaving} \} \]
\[ = \{ \text{Property 1.0, the distribution of projection over concatenation, using} \} \]
\[ \epsilon \in S \setminus \epsilon \text{ and } b \in S \setminus \epsilon \}
\[ s a b t \in (a S \cup a T)^* \land s b a t \in t S \land s a t \in t T \]
\[ = \{ \text{Distribution of projection over concatenation, using} \} \]
\[ a \in S \setminus \epsilon \text{ and } b \in S \setminus \epsilon \}
\[ s a b t \in (a S \cup a T)^* \land s b a t \in t S \land s a t \in t T \]
\[ = \{ \text{definition of weaving} \} \]
\[ s b a t \in t(S \cdot w T) \]

(End of Proof)

Property 1.4: Weaving is symmetric.

Property 1.5: The trace set of the weave of two trace structures with prefix-closed trace sets is prefix-closed.

The second operator that we discuss is blending. A weave still reflects the composite's internal structure. By projection on the alphabets of the composing trace structures, the individual traces from which the traces of the composite are formed can be retrieved. After projection on the symmetric difference of the alphabets of the composing trace structures the internal communications are hidden. This blend of two trace structures \( S \) and \( T \), denoted by \( S \cdot b T \), is the trace structure.
\[(S \triangle T) \setminus (aS \cup aT)\]

where \(\triangle\) denotes symmetric set difference. Input and output alphabet of \(S \cup T\) are defined as \(i(S \triangle T)\) and \(o(S \triangle T)\) respectively.

**Property 1.6**: Blending is symmetric.

**Example 1.2** (cf. Example 1.1)

\[
\langle \{ ab, abc, de \} , \{ a, b, d, e \} \rangle \leq \langle \{ bc, bce, fe \} , \{ b, c, e, f \} \rangle = \\
\langle \{ ac, af, ef \} , \{ a, c, d, f \} \rangle
\]

(End of Example)

**Property 1.7**: The trace set of the blend of two trace structures with prefix-closed trace sets is prefix-closed.

**Property 1.8**: For trace structures \(S\) and \(T\) and for trace \(s\)

\[s \in t(S \triangle T) \Rightarrow s \in t(S \setminus aT) \cup tS \setminus (aS \setminus aT)\]

**Proof**:

\[
s \in t(S \triangle T)
\]

\[= \{ \text{definition of blending} \}
\]

\[\exists s_0 : s_0 \in t(S \triangle T) \wedge s_0 \in (aS \setminus aT) = s
\]

\[\Rightarrow \{ \text{definition of weaving} \}
\]

\[\exists s_0 : s_0 \in tS \setminus s_0 \in (aS \setminus aT) = s
\]

\[\Rightarrow \{ \text{projection on } aS \setminus aT \text{ and Property 1.1, using}
\]

\[aS \cap (aS \setminus aT) = (aS \setminus aT) \cap (aS \setminus aT)
\]

\[\exists s_0 : s_0 \in (aS \setminus aT) \setminus (aS \setminus aT) \wedge tS \setminus (aS \setminus aT) = s
\]

\[\Rightarrow \{ \text{calculus} \}
\]

\[s \in (aS \setminus aT) \setminus aS \setminus aT
\]

(End of Proof)
1.1. A program notation

In this section we discuss a way to represent trace structures. Since trace sets are
often infinite, a representation by enumeration of its elements becomes rather
cumbersome. We use so-called commands with which we associate trace struc-
tures.

With command $S$ trace structure $\text{TR}S$ is associated in the following way.

- A symbol is a command. For symbol $a$ \[ \text{TR}a = \langle \{ a \}, \{ a \} \rangle. \]

- If $S$ and $T$ are commands then $(S \mid T)$ is a command.
  \[ \text{TR}(S \mid T) = \langle t(\text{TR}S) \cup t(\text{TR}T), a(\text{TR}S) \cup a(\text{TR}T) \rangle. \]

- If $S$ and $T$ are commands then $(S ; T)$ is a command. \[ \text{TR}(S ; T) = \langle \{ x, y : x \in t(\text{TR}S) \land y \in t(\text{TR}T) \} \cup xy, a(\text{TR}S) \cup a(\text{TR}T) \rangle. \]

- If $S$ and $T$ are commands then $(S, T)$ is a command.
  \[ \text{TR}(S, T) = (\text{TR}S) \cup (\text{TR}T). \]

- If $S$ is a command then $S^*$ is a command.
  \[ \text{TR}(S^*) = \langle t(\text{TR}S)^*, a(\text{TR}S) \rangle. \]

Furthermore, there are a few priority rules. The star has the highest priority, fol-
lowed by the comma, the semicolon, and the bar. The trace sets thus obtained
are not prefix-closed. Since we are interested in prefix-closed trace sets only, as
will turn out in the next chapter, we associate with a command $S$ the trace
structure $\langle \text{pref}(\text{TR}S), a(\text{TR}S) \rangle$, when the command is used for the
specification of a mechanism.

Example 1.3

The specification of a Wire, as exemplified in Example 1.0 would be: input
alphabet $\{ a \}$, output alphabet $\{ b \}$, and command $(a ; b)^*.$

(End of Example)

Example 1.4

A Muller-C element, or C-element for short [6], is an element with two inputs
and one output. It is supposed to synchronize the inputs, i.e. after having
received an input change on both input wires, it produces a change on the out-
put wire. Its specification is a trace structure with input alphabet $\{ a, b \}$, output
alphabet $\{ c \}$, and command $(a, b ; c)^*.$

(End of Example)
Classification of delay-insensitive trace structures

With the trace theory as introduced in the preceding chapter we are now able to define delay-insensitive trace structures formally. We reserve the term component for a mechanism that is an abstraction of an electrical circuit. A trace structure is the specification of the communications between a component and its environment. Inputs of the trace structure are inputs with respect to the component and outputs with respect to the environment. Outputs of the trace structure are outputs with respect to the component and inputs with respect to the environment.

The key to the definition of delay-insensitive trace structures is the component and its environment being insensitive to the speeds with which they operate and to propagation delays in connecting wires. This is informally captured by viewing a component as being wrapped in some kind of foam box representing a flexible and possibly time-varying boundary. The communication actions between component and environment are specified at this boundary. The flexibility of this boundary imposes certain restrictions that the specification of a delay-insensitive circuit has to satisfy. As will turn out in the sequel, these requirements basically amount to the absence of ordering between certain symbols: the presence of certain traces in a trace structure's trace set implies the presence of other traces in that trace set. It is not a priori obvious that the requirements deduced in this chapter on account of this foam rubber wrapper principle are sufficient to guarantee proper communications. This will only turn out in Chapter 4.

The first restriction to be imposed upon a trace structure is that its alphabet be partitioned into an input and an output alphabet. We do not, at this level of abstraction at least, consider a communication means other than input or output, nor do we consider ports that are input at one time and output at another.
time. This means that we have for trace structure $T$ the rule

$$ R_a) \quad i\ T \cup o\ T = a\ T $$

Notice that $i\ T \cap o\ T = \emptyset$ according to the definition of a trace structure.

Second, we impose the restriction that a trace set be prefix-closed and non-empty. This rule is dictated by the fact that a system that can produce trace $t\ a$ is assumed to do so by first producing $t$ and then $a$. The symbols in a trace structure's alphabet are viewed as atomic actions. Moreover, a system must be able to produce $\epsilon$ initially. This gives for trace structure $T$ the rule

$$ R_b) \quad t\ T \text{ is prefix-closed and non-empty} $$

The basic idea of this monograph is that we do not make any assumptions on absolute or relative wire delays. As we pointed out in the introduction, this leads to the assumption of a transition being monotonic in order to enable a component to recognize the signal that this transition represents. This means that we have to guarantee transitions against interference and, therefore, have to limit the number of transitions on a wire to at most one. In terms of trace structures, where signals via the same wire are represented by the same symbol, this amounts to the restriction that adjacent symbols be different. This gives for trace structure $T$ the following necessary condition.

$$ R_{sa}) \quad \text{for trace } s \text{ and symbol } a \in a\ T \quad saa \notin t\ T $$

Signals are sent in either of two directions, viz. from a component to its environment or the other way round. Due to unknown wire delays, two signals being sent the one after the other in the same direction via different wires need not be received in the order in which they are sent. In other words, we cannot assume our communications to be order preserving. Consequently, a specification of a delay-insensitive component does not depend on the order in which this kind of concurrent signals is sent or received. Therefore, a trace structure containing a trace with two adjacent symbols of the same type (input or output) also contains the trace with these two symbols swapped. In fact, we conceive adjacent symbols of the same type as not being ordered at all. (Their occurrence as adjacent symbols in a trace is just a shortcoming of our writing in a linear way.) For trace structure $T$, this is expressed by the following restriction

$$ R_{sabb}) \quad \text{for traces } s \text{ and } t, \text{ and for symbols } a \in a\ T \text{ and } b \in a\ T \text{ of the same type } sabb \notin t\ T = sbat \in t\ T $$

Due to the fourth rubber wrapper principle, signals in opposite directions are subject to restrictions as well. As opposed to signals of the same type, they may
have a causal relationship and, hence, have an order. If, however, in some phase of the computation they are not ordered, meaning that for some trace \( s \) and symbols \( a \) and \( b \) both \( sa \in tT \) and \( sb \in tT \), then the traces that \( sba \) and \( sab \) can be extended with, according to the component's trace set, should not differ too much. Obviously, we do justice to the foam rubber wrapper principle if the order of this kind of concurrent symbols is of no importance at all. This results for trace structure \( T \) in the rule

\[
R_4' \quad \text{for traces } s \text{ and } t, \text{ and for symbols } a \in aT \text{ and } b \in aT \text{ of different types}
\]

\[
sa \in tT \land sba \in tT \Rightarrow sba \in tT
\]

Finally, we have to take into account that a signal, once sent, cannot be cancelled. However long it takes, eventually it will reach its destination. Consequently, a component ready to receive a certain signal from its environment, which means that the trace thus far generated extended with that symbol belongs to the trace set, must not change its readiness when sending a signal to its environment. In other words, in the absence of an oracle informing either side on signals that, though possible, will not be sent, we cannot allow in a specification that a symbol disables a symbol of another type. Symbol \( a \) disables symbol \( b \) in trace structure \( T \) if there is a trace \( s \) with

\[
sa \in tT \land sb \in tT \land sab \in tT
\]

There is nothing wrong, however, with symbols that disable symbols of the same type. If these symbols are input symbols then the environment has to make a decision which output symbol(s) to send. If, on the contrary, the symbols are output symbols then the component has to make that decision. Since a correct use of arbitration devices is one of the important incentives to the study of delay-insensitive circuits, the various types of decisions are a key to the classification. Three classes, each of them described by one of the following non-disabling rules, can be distinguished now. For trace structure \( T \) we have

\[
R_5' \quad \text{for trace } s \text{ and distinct symbols } a \in aT \text{ and } b \in aT
\]

\[
sa \in tT \land sb \in tT \Rightarrow sab \in tT
\]

\[
R_5'' \quad \text{for trace } s \text{ and distinct symbols } a \in aT \text{ and } b \in aT, \text{ not both input symbols}
\]

\[
sa \in tT \land sb \in tT \Rightarrow sab \in tT
\]

\[
R_5''' \quad \text{for trace } s \text{ and symbols } a \in aT \text{ and } b \in aT \text{ of different types}
\]

\[
sa \in tT \land sb \in tT \Rightarrow sab \in tT
\]

All delay-insensitive trace structures satisfy \( R_0 \) through \( R_5 \). The class satisfying \( R_4' \) and \( R_5' \) as well is called the synchronization class. It is also denoted by \( C_1 \). A specification in this class allows for synchronization only. Due to the
absence of decisions, no data transmission is possible. The class allowing for
input symbols to be disabled, satisfying therefore $R_5'$ and $R_5''$, is called the data
communication class. It is also denoted by $C_2$. Here the data is encoded by
means of the possible decisions. Finally, we have $C_3$, or the arbitration class,
which allows a component to choose between output symbols. Specifications in
this class satisfy, in addition to $R_0$ through $R_3$, $R_4'$ and $R_5''$. Obviously,
$C_1 \subseteq C_2 \subseteq C_3$.

We could have distinguished the class in which decisions are made in the
component and not in the environment, which is $C_3$ with in its $R_5''$ the restric-
tion ‘not both inputs’ replaced by ‘not both outputs’. We have not done so, how-
ever, since none of the classes thus obtained turns out to be closed under the
composition operator proposed in the next chapter, a circumstance making none
of these classes very interesting. $C_3$ has, arbitrarily, been chosen to demon-
strate this phenomenon.

The reason that $C_3$ is not closed under composition is that $R_4'$ is too restric-
tive in the presence of decisions in the component, as is shown in Chapter 5. We
concluded the analysis for $R_4'$ by observing that the foam rubber wrapper prin-
ципe would certainly be done justice if the order of concurrent symbols of
different types was of no importance. This situation, however, needs a more care-
ful analysis.

The specification of a component must not depend on the place of the bound-
dary of the foam rubber wrapper. Consider two wrappers, the one contained in
the other one. If, at the outside boundary the order between two concurrent
input and output signals is input-before-output, then nothing can be said about
their order at the inside boundary. If, on the other hand, the order between such
signals is output-before-input at the outside boundary, then the same order
between these symbols is implied at the inside boundary.

The first situation, i.e. input-before-output at the outside boundary, gives rise
to a restriction to be imposed upon a component’s trace set. Assume that we
have traces $s$ and $t$, input symbol $a$, output symbol $b$, and traces $sabt$ and $sbat$
in the component’s trace set. Trace $sabt$ is the trace associated with the outside
boundary and trace $sbat$ is the one that is associated with the inside boundary.
Now if $sabt$ can be extended -according to the component’s trace set- with an
input symbol $c$, which means a signal from the outside boundary towards the
inside boundary, then a necessary condition for absence of computation interfer-
ence at the inside boundary is the presence of trace $sabc$ in the component’s
trace set.

A similar observation applies to an output-before-input order of concurrent
symbols at the inside boundary and an input-before-output order at the outside
boundary. In this case an output signal possible at the inside boundary should
be possible at the outside boundary as well. This results for trace structure $T$ in
the following rule, which is less restrictive than $R_4'$.
\( R_4'' \) for traces \( s \) and \( t \), and for symbols \( a \in aT \), \( b \in aT \), and \( c \in aT \) with \( b \)
of another type than \( a \) and \( c \):

\[
s | tT \land sbat \in tT \Rightarrow sbate \in tT
\]

\( R_3 \) through \( R_5 \) together with \( R_4'' \) and any of the three \( R_6 \)'s constitute a class
of delay-insensitive trace structures. We give a name to the largest class only,
which is the one with \( R_4'' \) and \( R_5'' \). We call it the class of delay-insensitive trace
structures and denote it by \( C_4 \). Obviously, \( C_3 \subseteq C_4 \). We do not attach
names to the other classes, since these classes neither provide more insight nor
have surprising properties.

Before exploring \( R_4'' \), we illustrate this classification by a number of examples.
In these examples we sometimes represent a trace structure by a state
graph instead of by a command. A state graph is a directed graph with one spe-
cial node, the start node, and arcs labelled with symbols of the trace structure’s
alphabet. Each path from the start node corresponds to a trace, viz. the one that
is brought about by the labels of the consecutive arcs in that path. A state graph
is said to represent a trace structure if it has the same trace set as that trace
structure. Rules \( R_3 \), \( R_4' \), and \( R_5 \) are usually more easily checked in a state
graph than in a command. Rule \( R_4'' \) is hard to check in either representation.
In the figures the start nodes are drawn fat. Choosing another node as start node
means another initialization of the component. Components that only differ
from one another by different start nodes are given the same name. For clear-
ness’ sake we attach a question mark to arcs labelled with an input symbol and
an exclamation mark to arcs labelled with an output symbol.

**Example 2.0**

The Wire and the C-element of Examples 1.3 and 1.4 are \( C_1 \)'s. Interchanging
the roles of the input and the output alphabet yields \( C_1 \)'s again. The wire
remains a wire, now starting with an output however. The C-element becomes a
Fork, viz. a trace structure with input alphabet \( \{ c \} \), output alphabet \( \{ a, b \} \),
and command \( (a, b; c)'' \). By another initialization we also have the command
\( (c; a, b)'' \) for a Fork.

(End of Example)

**Example 2.1**

Another very common element is the so-called Merge. It is an element with
input alphabet \( \{ a, b \} \), output alphabet \( \{ c \} \), and command \( (a \lor b); c)'' \). This component is a \( C_2 \), since inputs \( a \) and \( b \) disable one another. Interchanging the
roles of input and output alphabet yields a \( C_3 \). This is the simplest form of an
arbiter.

(End of Example)
Example 2.2
A C-element with two outputs instead of one is another example of a $C_1$. It has input alphabet \( \{ a, b \} \) and output alphabet \( \{ c, d \} \). There are two essentially different trace structures that synchronize the input signals. The first one is the C-element with its output symbol replaced by two output symbols in any order. This yields command \( (a, b ; c, d)^{\ast} \). In this trace structure we can distinguish an input and an output phase. Another command allows the two phases to overlap a little bit, but still synchronizes the inputs. This is expressed in the command \( a, b ; ((c, a), (d, b))^{\ast} \). (End of Example)

Example 2.3
Consider a C-element with input alphabet \( \{ a, r \} \), output alphabet \( \{ p \} \), and command \( (a, r ; p)^{\ast} \) and consider a Wire with input alphabet \( \{ q \} \), output alphabet \( \{ b \} \), and command \( (q, b)^{\ast} \). The Wire can be used to acknowledge the reception of symbol \( p \) by the environment before a next input \( a \) is allowed to occur. The resulting component has input alphabet \( \{ a, q, r \} \), output alphabet \( \{ b, p \} \), and command \( a ; (p, (q, b, a), r)^{\ast} \). We have chosen this initialization, since the component will be used in this form in Chapter 5. It is a $C_1$.
(End of Example)

Example 2.4
Another component that will be used in Chapter 5 is a component that can be thought of as consisting of three wires: two wires to convey a bit of information and one wire for the acknowledgement of its arrival. A bit is encoded as sending a signal on one of the two wires that are used for the data transmission. Its input alphabet is \( \{ x_0, x_1, b \} \), its output alphabet is \( \{ y_0, y_1, a \} \), and its command is \( (x_0, y_0, b ; a | x_1 ; y_1 ; b ; a)^{\ast} \). Because of the choice to be made between the inputs \( x_0 \) and \( x_1 \), this component is a $C_2$.
(End of Example)

Example 2.5
A parity counter is a component that counts the parity of a number of consecutive inputs. The parity can be retrieved on request an unbounded number of times. The symbol whose occurrences we want to count is \( x \). Its reception by the component is acknowledged by symbol \( a \). By means of symbol \( b \) we can retrieve the parity of the occurrences of \( x \) so far. Symbol \( y_0 \) represents an even number and symbol \( y_1 \) an odd number of occurrences. The trace structure's input
alphabet is \( \{ x, b \} \), its output alphabet is \( \{ a, y_0, y_1 \} \), and its command is \( ((b ; y_0)^* ; x ; a ; (b ; y_1)^* ; x ; a)^* \). This component is a C_2. There is a choice to be made between inputs \( x \) and \( b \). To show that more clearly we draw a state graph of this component.

Any two arcs from the same node have labels of the same type, which implies that \( R_4' \) is trivially satisfied. There are no two consecutive arcs with labels of the same type, which implies that \( R_5 \) is satisfied. Any two arcs from the same node have labels of type input that do disable one another. This does not meet requirement \( R_5' \), but this is allowed according to \( R_4'' \). Consequently, this is a C_2.

(End of Example)

**Example 2.6**

An And-element with input alphabet \( \{ a, r \} \) and output alphabet \( \{ c \} \) is quite often used in the following way. Both inputs go high in some order whereby the output follows the inputs. Next, both inputs go low again and the output follows the first low-going input transition. This is expressed by the command \( (a ; r ; c ; (a ; c ; r) ; r ; (a ; c))^* \). This trace structure is not delay-insensitive, however. It contains, for instance, the trace \( aacrac \), which violates \( R_2 \). It can be made delay-insensitive by replicating both inputs. Then its input alphabet is \( \{ a, r \} \), its output alphabet \( \{ b, c, p \} \) and a possible command \( (a ; p ; r ; b ; c ; a ; c ; (p ; r ; b))^* \). Input \( a \) is now acknowledged by \( p \) and \( r \) by \( b \). It is not the most general command for a delay-insensitive And-element but one that suffices for the sequel. The corresponding trace structure is a C_1.

(End of Example)

**Example 2.7**

A binary variable is a component that can store one bit of information, which may be retrieved afterwards on request an unbounded number of times. The component has input alphabet \( \{ x_0, x_1, b \} \), output alphabet \( \{ y_0, y_1, c \} \), and command \( (x_0 ; a ; (b ; y_0)^* ; x_1 ; a ; (b ; y_1)^* \). Symbol \( a \) acknowledges the reception of a bit (either \( x_0 \) or \( x_1 \)), and \( b \) is the request for the currently stored value. A state graph looks like.
In the start node a choice has to be made between \( x_0 \) and \( x_1 \) (it has no currently stored value). Moreover, there are two nodes where a choice has to be made between inputs \( b \), \( x_0 \), and \( x_1 \). This makes it a \( C_2 \).

(End of Example)

Example 2.8

A buffer is an element that allows us to store a series of values and to retrieve them in the same order. Usually a buffer has a finite number of places for storage, which bounds the number of values that can be stored simultaneously.

In this example we discuss a one-place one-bit buffer. The reception of one bit, either \( x_0 \) or \( x_1 \), is acknowledged by \( a \). Symbols \( y_0 \) and \( y_1 \) are used to return the stored value. Symbol \( b \) signals the environment's readiness (or request) for the next value. Initially the environment is ready to receive a value. There exist less complicated buffers, more similar to the variable of the preceding example. We have chosen for this buffer and this initialization, since this buffer can easily be composed with another one as will turn out in Chapters 3 and 5. The trace structure of this component has input alphabet \( \{ x_0, x_1, b \} \), output alphabet \( \{ y_0, y_1, a \} \), and command

\[
\begin{align*}
x_0; 
((a; x_0),(y_0; b))^*; (a; x_1),(y_0; b); ((a; x_1),(y_1; b))^*; (a; x_0),(y_1; b))^* \mid \quad \text{and command} \\
x_1; 
((a; x_0),(y_1; b))^*; (a; x_0),(y_1; b); ((a; x_0),(y_0; b))^*; (a; x_1),(y_0; b))^*
\end{align*}
\]

A state graph looks like
We have not labelled all arcs. Opposite sides of the parallelogram have equal labels. Nodes that have been attached the same number are identical. Here we see the existence of a node with outgoing arcs with labels of different types. It is easy to see that $R_4'$ is still satisfied, since arcs with such labels make up a parallelogram, which means that their order is of no importance. This component is a $C_2$, the only decision to be made being the one between inputs $r_0$ and $r_1$.

(End of Example)

Example 2.9
An arbiter, in one of its simplest forms, grants one out of two requests. The arbiter that we discuss in this example has a cyclic way of operation, i.e. it needs both requests before being able to deal with the next request. It has input alphabet $\{a, b\}$ and output alphabet $\{c, p, q\}$. In every cycle exactly one of the outputs $p$ and $q$ changes. A change in $a$ precedes a change in $p$ and, likewise, a change in $q$ is preceded by a change in $b$. The output $c$ signals the completion of the cycle after reception of $a$ and $b$. Consequently, the command is $((a, b ; c), ((a ; p), b | (b ; q), a))^*$. A state graph is

This component is a $C_3$, the choice to be made being the one between outputs $p$ and $q$. Notice that this specification does not exhibit a first come first serve principle. In delay-insensitive trace structures such a principle cannot be expressed. A realization of this component may exhibit a first come first serve behaviour, however.

(End of Example)

Example 2.10
In the arbiter of this example an additional symbol $r$ is introduced that signals the reception by the environment of either $p$ or $q$. Moreover, $c$ is postponed until after the reception of $r$. For reasons explained in the next chapter we sometimes prefer this arbiter to the one in Example 2.9. The input alphabet of this component is $\{a, b, r\}$, the output alphabet is $\{c, p, q\}$, and the command is
\((a, b, r; c)^*, (a; p; r), b \mid (b; q; r), a)^*\). A state graph, from which it can be seen that this component is a \(C_3\), is

![State Graph](image)

(End of Example)

**Example 2.11**

The arbiter in this example allows multiple requests of one kind of symbol, e.g., \(a\), without the need for the occurrence of the other symbol, \(b\) in this case. Its input alphabet is \(\{a, b\}\) and its output alphabet \(\{p, q\}\). A request, for a shared resource for example, is a high-going transition on one of the inputs \(a\) or \(b\). A high-going transition on \(p\) means that request \(a\) has been granted and, similarly, a high-going transition on \(q\) that \(b\) has been granted. At most one request will be granted at a time. A low-going transition on the input whose request had been granted signals the release of the shared resource whereafter a low-going transition on the output that granted this request makes the arbiter ready for a next request of the same kind. The state graph, from which it can be seen that this component is a \(C_3\), is

![State Graph](image)

(End of Example)
Example 2.12

The component of this example is used to demonstrate that \( C_2 \) is not closed under the composition operator to be introduced in the next chapter. It has input alphabet \( \{ a, d, e \} \), output alphabet \( \{ b, c, f \} \), and command

\[
(((f ; a), (b ; d)) ; f ; a ; (c ; e ; b ; d)) ; (b ; d)
\]

A state graph of this component is

(End of Example)

We conclude this chapter with a number of lemmata. Lemmata 2.0 through 2.7 deal with a generalization of \( R_s'' \). In Lemmata 2.8 through 2.11 we prove a few properties of \( C_2 \)'s in particular with respect to the shifting of output symbols to the right and input symbols to the left in traces of a \( C_2 \).

Lemma 2.0 : For \( T \) a \( C_s \), for traces \( s \) and \( t \), and for symbols \( a \) and \( b \) such that \( b \) is of another type than \( a \) and the symbols of \( t \)

\[
sh \in t T \land salt \in t T \Rightarrow sht \in t T
\]

Proof : By mathematical induction on the length of \( t \).

Base : \( t = \epsilon \).

\[
sh \in t T \land salt \in t T
\Rightarrow \{ t T \text{ is prefix-closed} \}

\[
sh \in t T \land salt \in t T
\Rightarrow \{ R_3'' \text{, using that } a \text{ and } b \text{ are of different types} \}

\[
shs \in t T
\Rightarrow \{ t = \epsilon \}

\[
sht \in t T
\]

Step : \( t = t \delta \). Hence, we have
\( b \) is of another type than \( e \) and the symbols of \( t_0 \) \( (0) \)

\[
\begin{align*}
\text{sb} \in tT \wedge \text{subt} \in tT \\
= \{ t = t_{\text{ge}} \text{ and } tT \text{ is prefix-closed} \}
\end{align*}
\]

\[
\begin{align*}
\text{sb} & \in tT \wedge \text{subt} \in tT \wedge \text{subt}_{\text{ge}} \in tT \\
\Rightarrow \{ \text{induction hypothesis, using (0)} \}
\end{align*}
\]

\[
\begin{align*}
\text{subt}_{\text{ge}} & \in tT \\
= \{ t = t_{\text{ge}} \}
\end{align*}
\]

\( \text{subt} \in tT \)

(End of Proof)

**Lemma 2.1**: For \( T \) a \( G_4 \), for traces \( s \) and \( t \), and for symbol \( b \) of another type than the symbols of \( t \)

\[
\text{sb} \in tT \wedge st \in tT \Rightarrow sbt \in tT
\]

**Proof**: By mathematical induction on the length of \( t \).

**Base**: \( t = e \). Obvious.

**Step**: \( t = at_0 \). Hence, we have

\( b \) is of another type than \( a \) and the symbols of \( t_0 \) \( (0) \)

\[
\begin{align*}
\text{sb} & \in tT \wedge st \in tT \\
= \{ t = a_{t_0} \text{ and } tT \text{ is prefix-closed} \}
\end{align*}
\]

\[
\begin{align*}
\text{sb} & \in tT \wedge sa \in tT \wedge sat_0 \in tT \\
\Rightarrow \{ \text{R}_a^{\prime}, \text{using (0)} \}
\end{align*}
\]

\[
\begin{align*}
\text{sb} & \in tT \wedge \text{subt} \in tT \\
\Rightarrow \{ \text{induction hypothesis, using (0)} \}
\end{align*}
\]

\[
\begin{align*}
\text{sb} & \in tT \wedge \text{subt}_{t_0} \in tT \\
\Rightarrow \{ \text{Lemma 2.0, using (0)} \}
\end{align*}
\]

\[
\begin{align*}
\text{subt}_{t_0} & \in tT \\
\Rightarrow \{ t = at_0 \}
\end{align*}
\]
\[ sbt \in tT \]

(End of Proof)

**Lemma 2.2**: For \( T \) a \( \mathcal{C}_4 \), for traces \( s \) and \( t \), and for symbol \( b \) such that \( b \) is of another type than the symbols of \( t \)

\[ sb \in tT \land st \in tT \Rightarrow (\forall w_0, w_1 : w_0w_1 = t : sw_0bw_1 \in tT) \]

**Proof**: By mathematical induction on the length of \( t \).

**Base**: \( t = \varepsilon \). Obvious.

**Step**: \( t = at_0 \). Hence, we have

\[ b \text{ is of another type than } a \text{ and the symbols of } t_0 \]  

(0)

\[ sb \in tT \land st \in tT \]

= \{ Lemma 2.1. Moreover, \( t = at_0 \) and \( tT \) is prefix-closed \}

\[ sbt \in tT \land sb \in tT \land st_0 \in tT \land s \in tT \]

\[ \Rightarrow \{ \text{R}_2''', \text{using (0)} \} \]

\[ sbt \in tT \land st_0 \in tT \land s \in tT \]

\[ \Rightarrow \{ \text{induction hypothesis, using (0)} \} \]

\[ sbt \in tT \land (\forall w_0, w_1 : w_0w_1 = t : sw_0bw_1 \in tT) \]

\[ = \{ \text{calculation} \} \]

\[ sbt \in tT \land (\forall w_0, w_1 : w_0w_1 = at_0 : sw_0bw_1 \in tT) \]

\[ = \{ t = at_0 \text{ and replacing } w_0 \text{ by } w_0 \} \]

\[ sbt \in tT \land (\forall w_0, w_1 : w_0w_1 = at_0 : sw_0bw_1 \in tT) \]

\[ = \{ \text{calculation} \} \]

\[ (\forall w_0, w_1 : w_0w_1 = t : sw_0bw_1 \in tT) \]

(End of Proof)

**Lemma 2.3**: For \( T \) a \( \mathcal{C}_4 \), for traces \( s, t, \) and \( u \), and for symbols \( a \) and \( \varepsilon \) such that \( a \) and \( \varepsilon \) are of another type than the symbols of \( t \)

\[ (\forall w_0, w_1 : w_0w_1 = t : sw_0uw_1 \in tT) \land s \in u \in tT \]

\[ \Rightarrow (\forall w_0, w_1 : w_0w_1 = t : sw_0uw_1 \in tT) \]
Proof: By mathematical induction on the length of $t$.

Base: $t = \epsilon$. Obvious.

Step: $t = b_0$. Hence, we have

$a$ and $\epsilon$ are of another type than $b$ and the symbols of $t_0$

\[(\forall w_0, w_1 : \omega_{\mathcal{G}_4} w_1 = t : \mathbf{su}_{\mathcal{G}_4} w_1 \in t T) \land \mathbf{satu} \in t T\]

\[= \{ t = b_0 \}\]

\[(\forall w_0, w_1 : \omega_{\mathcal{G}_4} w_1 = b_0 : \mathbf{su}_{\mathcal{G}_4} w_1 \in t T) \land \mathbf{satu} \in t T\]

\[\Rightarrow \{ \text{calculus} \}\]

\[(\forall w_0, w_1 : \omega_{\mathcal{G}_4} w_1 = t_0 : \mathbf{su}_{\mathcal{G}_4} w_1 \in t T) \land \mathbf{satu} \in t T \land \mathbf{satu} \in t T\]

\[\Rightarrow \{ \mathbf{R}_4^\ast, \text{using (0)} \}\]

\[(\forall w_0, w_1 : \omega_{\mathcal{G}_4} w_1 = t_0 : \mathbf{su}_{\mathcal{G}_4} w_1 \in t T) \land \mathbf{satu} \in t T \land \mathbf{satu} \in t T\]

\[\Rightarrow \{ \text{induction hypothesis, using (0)} \}\]

\[(\forall w_0, w_1 : \omega_{\mathcal{G}_4} w_1 = t_0 : \mathbf{su}_{\mathcal{G}_4} w_1 \in t T) \land \mathbf{satu} \in t T\]

\[\Rightarrow \{ \text{calculus} \}\]

\[(\forall w_0, w_1 : \omega_{\mathcal{G}_4} w_1 = b_0 \land w_0 \neq \epsilon : \mathbf{su}_{\mathcal{G}_4} w_1 \in t T) \land \mathbf{satu} \in t T\]

\[\Rightarrow \{ \text{calculus and } t = b_0 \}\]

\[(\forall w_0, w_1 : \omega_{\mathcal{G}_4} w_1 = t : \mathbf{su}_{\mathcal{G}_4} w_1 \in t T)\]

(End of Proof)

In exactly the same way we derive

Lemma 2.4: For $T$ a $\mathcal{G}_4$, for traces $s$, $t$, and $u$, and for symbols $b$ and $t$ such that $b$ is of another type than $t$ and the symbols of $t$

\[(\forall w_0, w_1 : \omega_{\mathcal{G}_4} w_1 = t : \mathbf{su}_{\mathcal{G}_4} w_1 \in t T) \land \mathbf{satu} \in t T\]

\[\Rightarrow (\forall w_0, w_1 : \omega_{\mathcal{G}_4} w_1 = t : \mathbf{su}_{\mathcal{G}_4} w_1 \in t T)\]

Lemma 2.5: For $T$ a $\mathcal{G}_4$, for traces $s$, $t$, and $u$, and for symbol $a$ such that $a$ is of another type than the symbols of $t$

\[\mathbf{su} \in t T \land \mathbf{satu} \in t T \Rightarrow (\forall w_0, w_1 : \omega_{\mathcal{G}_4} w_1 = t : \mathbf{su}_{\mathcal{G}_4} w_1 \in t T)\]

Proof: By mathematical induction on the length of $u$. 
Base : $u = c$.

$sat_u \in t \mathbb{T} \land sat_u \in t \mathbb{T}$

$\Rightarrow$ ( $t \mathbb{T}$ is prefix-closed )

$sat_u \in t \mathbb{T} \land sat_u \in t \mathbb{T}$

$\Rightarrow$ ( Lemma 2.2, since $a$ is of another type than the symbols of $t$ )

$(\forall w_0, w_1 : w_0 w_1 = t : sat_u w_0 w_1 \in t \mathbb{T})$

$\Rightarrow$ ( $u = \epsilon$ )

$(\forall w_0, w_1 : w_0 w_1 = t : sat_u w_0 w_1 \in t \mathbb{T})$

Step : $u = u_0 b$.

$sat_u \in t \mathbb{T} \land sat_u \in t \mathbb{T}$

$= \{ u = u_0 b$ and $t \mathbb{T}$ is prefix-closed $\}$

$sat_u \in t \mathbb{T} \land sat_u \in t \mathbb{T} \land sat_u \in t \mathbb{T} \land sat_u \in t \mathbb{T}$

$\Rightarrow$ ( induction hypothesis )

$(\forall w_0, w_1 : w_0 w_1 = t : sat_u w_0 w_1 \in t \mathbb{T}) \land sat_u \in t \mathbb{T} \land sat_u \in t \mathbb{T}$

$\Rightarrow$ ( Lemma 2.3 if the types of $a$ and $b$ are equal, Lemma 2.4 if they are not )

$(\forall w_0, w_1 : w_0 w_1 = t : sat_u w_0 w_1 \in t \mathbb{T})$

$= \{ u = u_0 b \}$

$(\forall w_0, w_1 : w_0 w_1 = t : sat_u w_0 w_1 \in t \mathbb{T})$

(End of Proof)

Lemma 2.6 : For $T$ a $\mathbb{C}_4$, for traces $s$, $t$, and $u$, and for symbols $a$ and $c$ such that the symbols of $t$ are of another type than $a$ and $c$

$sat_u \in t \mathbb{T} \land sat_u \in t \mathbb{T} \Rightarrow sat_u \in t \mathbb{T}$

Proof:

$sat_u \in t \mathbb{T} \land sat_u \in t \mathbb{T}$

$= \{ t \mathbb{T}$ is prefix-closed $\}$

$sat_u \in t \mathbb{T} \land sat_u \in t \mathbb{T} \land sat_u \in t \mathbb{T}$

$\Rightarrow$ ( Lemma 2.5 )

$(\forall w_0, w_1 : w_0 w_1 = t : sat_u w_0 w_1 \in t \mathbb{T}) \land sat_u \in t \mathbb{T}$

$\Rightarrow$ ( Lemma 2.3 )

$(\forall w_0, w_1 : w_0 w_1 = t : sat_u w_0 w_1 \in t \mathbb{T})$
\[ \Rightarrow \{ \text{ instantiation } \} \]

\[ s\text{true} \in t \; T \]

(End of Proof)

In a similar way, applying Lemma 2.4 instead of 2.3, we derive

Lemma 2.7 : For \( T \in \mathbb{C}_1 \), for traces \( s, t, u \), and \( u \) and for symbols \( b \) and \( c \) such that \( b \) is of another type than \( c \) and the symbols of \( t \)

\[ st\text{true} \in t \; T \land s\text{true} \in t \; T \Rightarrow s\text{false} \in t \; T \]

Finally we prove a few lemmata on the shifting of symbols in \( \mathbb{C}_2 \)’s.

Lemma 2.8 : For \( T \in \mathbb{C}_2 \), for traces \( s \) and \( t \), and for symbol \( a \in \sigma \; T \) such that \( t \{ \{ a \} \} = \epsilon \)

\[ sa \in t \; T \land st \in t \; T \Rightarrow sta \in t \; T \]

Proof : By mathematical induction on the length of \( t \).

Base : \( t = \epsilon \). Obvious.

Step : \( t = t \; b \). Hence, we have

\[ t_0 \{ \{ a \} \} = \epsilon \; \text{ and } \; a \not\equiv b \]

\[ sa \in t \; T \land st \in t \; T \]

\[ = \{ \; t = t \; b \; \text{ and } \; t \; T \; \text{ is prefix-closed} \} \]

\[ sa \in t \; T \land st_0 \in t \; T \land st_0 \in t \; T \]

\[ \Rightarrow \{ \; \text{ induction hypothesis, using (0)} \} \]

\[ st_0 \in t \; T \land st_0 \in t \; T \]

\[ \Rightarrow \{ \; \text{R}_5^\prime \; \text{, using } a \in \sigma \; T \; \text{ and } \; a \not\equiv b \; \text{ according to (0)} \} \]

\[ st_0 \in t \; T \]

\[ st \in t \; T \]

(End of Proof)
Lemma 2.9 : For $T$ a $C_2$, for traces $t$, $t_1$, and $u$, and for symbol $a \in o T$ such that $t[a] = \epsilon$
\phantom{a} \hspace{1cm} sa \in t T \land stan \in t T \Rightarrow sau \in t T$

Proof : By mathematical induction on the length of $t$.
Base : $t = \epsilon$. Obvious.
Step : $t = t_0 b$. Hence, we have
\begin{equation}
 t_0 [ \{ a \} ] = \epsilon \text{ and } a \neq b \tag{0}
\end{equation}

\begin{align*}
 & sa \in t T \land stan \in t T \\
 \Rightarrow & \{ t = t_0 b \text{ and } t T \text{ is prefix-closed } \} \\
 & sa \in t T \land st_0 \in t T \land st_0 ba u \in t T \\
 \Rightarrow & \{ \text{Lemma 2.8, using (0) and } a \in o T \} \\
 & sa \in t T \land st_0 u \in t T \land st_0 ba u \in t T \\
 \Rightarrow & \{ R_4 \text{ if } a \text{ and } b \text{ are of different types, } R_3 \text{ if they are of the same type } \} \\
 & sa \in t T \land st_0 ba u \in t T \\
 \Rightarrow & \{ \text{induction hypothesis, using (0)} \} \\
 & sat_0 b u \in t T \\
 \Rightarrow & \{ t = t_0 b \} \\
 & sau \in t T \\
\end{align*}
(End of Proof)

Lemma 2.10 : For $T$ a $C_3$, for traces $t$, $t_1$, and $u_1$, and for symbol $a \in o T$ such that $t[a] = \epsilon$
\phantom{a} \hspace{1cm} sa \in t T \land stan \in t T \Rightarrow (\forall u_0, u_1 : u_0 u_1 = t : st_0 u_0 u_1 u \in t T)$

Proof :
\begin{align*}
 & sa \in t T \land stan \in t T \\
 \Rightarrow & \{ t T \text{ is prefix-closed and calculus } \} \\
 & (\forall u_0, u_1 : u_0 u_1 = t : sa \in t T \land su_0 \in t T \land su_0 u_1 u \in t T) \\
 \Rightarrow & \{ \text{since } t[a] = \epsilon \text{, we have, if } u_0 u_1 = t, w_0[a] = \epsilon. \text{ Hence, we may apply Lemma 2.8 } \} \\
 & (\forall u_0, u_1 : u_0 u_1 = t : su_0 u_0 u_1 u \in t T) \\
\end{align*}
\[ \Rightarrow \{ \text{Lemma 2.9} \}
\]
\[ (\forall w_0, w_1 : w_0w_1 = t : sw_0sw_1u \in t T) \]

(End of Proof)

Lemma 2.11: For \( T \) a \( C_2 \), for traces \( s, t, u \), and for symbol \( a \in t T \)
\[ (\forall w_0, w_1 : w_0w_1 = t : sw_0a \in t T) \land sta \in t T \]
\[ \Rightarrow (\forall w_0, w_1 : w_0w_1 = t : sw_0au \in t T) \]

Proof: By mathematical induction on the length of \( t \).
Base: \( t = \epsilon \). Straightforward.
Step: \( t = t_0 \delta \). Then we derive
\[ (\forall w_0, w_1 : w_0w_1 = t : sw_0a \in t T) \land sta \in t T \]
\[ = \{ t = t_0 \delta \} \]
\[ (\forall w_0, w_1 : w_0w_1 = t_0 : sw_0a \in t T) \land sta \in t T \]
\[ \Rightarrow \{ \text{calculation} \} \]
\[ (\forall w_0, w_1 : w_0w_1 = t_0 : sw_0a \in t T) \land sta \in t T \land st_0au \in t T \]
\[ \Rightarrow \{ \text{R}_3 \text{ if } a \text{ and } b \text{ are of the same type. } \text{R}_4 \text{ if they are of different types } \} \]
\[ (\forall w_0, w_1 : w_0w_1 = t_0 : sw_0a \in t T) \land sta \in t T \land st_0au \in t T \]
\[ \Rightarrow \{ \text{induction hypothesis} \} \]
\[ (\forall w_0, w_1 : w_0w_1 = t_0 : sw_0auw_1u \in t T) \land sta \in t T \]
\[ = \{ \text{calculation} \} \]
\[ (\forall w_0, w_1 : w_0w_1 = t_0w_1w_0 \not= \epsilon : sw_0auw_1u \in t T) \land sta \in t T \]
\[ = \{ \text{calculation and } t = t_0 \delta \} \]
\[ (\forall w_0, w_1 : w_0w_1 = t : sw_0auw_1u \in t T) \]

(End of Proof)
Independent alphabets and composition

In this chapter we introduce so-called independent alphabets. Informally speaking, we partition the environment of a component in such a way that the subenvironments are mutually independent with respect to their communications with that component. Such a partitioning is, for example, a justification for sometimes conceiving the environment as being divided into a left and a right environment. In the last section a composition operator is defined using independent alphabets.

3.0. Independent alphabets

Outputs of the component are under control of the component and inputs of the component are under control of the environment. The component will operate according to its specification by sending outputs as long as the environment sends outputs that the component is able to receive according to that specification, in other words as long as there is absence of computation interference.

Composition of two electrical circuits usually involves the interconnection of just a subset of wires of the circuits to be composed. Communications via these wires are the composite's internal communications. The remaining wires are used for the external communications, i.e. the communications of the composite with its environment. Therefore, the environment of each component is partitioned, upon composition, into an environment for the internal and an environment for the external communications. This implies two so-called local specifications, viz. the one obtained by projecting the original specification onto the symbols used for the internal communications and the one obtained by projecting onto the symbols used for the external communications. A nice property of this partitioning would be that the internal and external communications could be carried
out according to the rules of the preceding paragraph just with respect to their local specifications, i.e. by locally guaranteeing absence of computation interference guaranteeing absence of computation interference for the whole. This is captured in the requirement that if an input symbol is allowed to occur according to a local specification then it is also allowed to occur according to the global one. Formally this is defined as follows.

**Definition 3.0**: For \( T \) a \( C_4 \), alphabet \( C, C \subseteq a^* T \), is independent with respect to \( T \) if

\[
(\forall s, a : s \in t T \land a \in C \cap i T : sa \in t T{\downarrow}C = sa \in t T) \land \\
(\forall s, a : s \in t T \land a \in C \cap i T : sa \in t T{\downarrow}C = sa \in t T)
\]

where the complement of \( C \) with respect to \( a^* T \) is denoted by \( \overline{C} \).

(End of Definition)

Notice that \( a^* T \) itself is independent with respect to trace structure \( T \). The equality could be replaced by an implication since \( sa \in t T \Rightarrow sa \in t T{\downarrow}C \) by definition. Moreover, it can be seen that independence of \( C \) is the same as independence of \( \overline{C} \).

One of the requirements for composition of two components will be that their set of common symbols be independent with respect to both components. This is sufficient to guarantee absence of computation interference as far as external input symbols are concerned, as will be proved in Lemma 3.5. Additional requirements are needed to guarantee absence of computation interference for the internal inputs. First, however, we illustrate the definition of the notion of independent alphabet using some examples of the preceding chapter.

**Example 3.0**

Consider a \( C \)-element with two output wires as in Example 2.2. The input alphabet is \( \{a, b\} \), the output alphabet is \( \{c, d\} \). The component with command \( a, b, ((c ; a), (d ; b))^{\ast} \) has independent alphabets \( \{a, c\} \) and \( \{b, d\} \). Projection on \( \{a, c\} \) yields a trace structure with command \( a ; c)^{\ast} \). The traces in this trace structure that contain an equal number of \( a \)'s and \( c \)'s may be extended with \( a \). Traces of the original trace structure with an equal number of \( a \)'s and \( c \)'s may be extended with \( a \) as well, as can easily be seen from the command. For reasons of symmetry, something similar holds for alphabet \( \{b, d\} \).

Taking the component with command \( a, b ; c, d \), however, one cannot find independent alphabets other than the trivial ones. Trace \( a b c d \) in this trace structure, for instance, cannot be extended with \( a \), although its projection on \( \{a, c\} \),
being $a$, may be extended with $a$ in the projection of the trace structure onto \{ $a$, $c$ \}.

(End of Example)

Example 3.1

The C-wire element of Example 2.3 with input alphabet \{ $a$, $q$, $r$ \}, output alphabet \{ $b$, $p$ \} and command $a \cdot (q ; b ; a) ; r$ has independent alphabets \{ $a$, $b$ \} and \{ $p$, $q$, $r$ \}. Projection on \{ $a$, $b$ \} yields a trace structure with command $(a ; b)$. As in the preceding example, the traces of this trace structure that contain an equal number of $a$'s and $b$'s may be extended with input $a$. The same holds for the traces of the original trace structure as can be seen from the command. Consequently, with respect to alphabet \{ $a$, $b$ \} the first of the two conditions of independence is met. Moreover, projection on \{ $p$, $q$, $r$ \} yields a trace structure with command $(p ; q , r)$' with output $p$ and inputs $q$ and $r$. The traces of this trace structure that have a lead of $p$ over $q$ may be extended with $q$ and traces that have a lead of $p$ over $r$ may be extended with $r$. The same holds for the traces in the original trace structure.

(End of Example)

Example 3.2

The three wires of Example 2.4 have independent alphabets as well. The input alphabet is \{ $x_0$, $x_1$, $b$ \}, the output alphabet is \{ $y_0$, $y_1$, $a$ \}, and the command is $(x_0 ; y_0 ; b ; a | x_1 ; y_1 ; b ; a)$. The alphabets \{ $x_0$, $x_1$, $a$ \} and \{ $y_0$, $y_1$, $b$ \} are independent. Symbol $a$ may immediately be followed by either $x_0$ or $x_1$, and symbols $y_0$ or $y_1$ by $b$. Notice that $x_0$ and $x_1$, which are two input symbols that disable one another, necessarily belong to the same independent alphabet. This is one of the reasons that the partitioning into the three wires \{ $x_0$, $y_0$, $x_1$, $y_1$ \}, and \{ $a$, $b$ \} does not yield independent alphabets. Notice also that, although the component is a $C_2$, the projection on independent alphabet \{ $y_0$, $y_1$, $b$ \} is a $C_3$. Nevertheless, we prove in Chapter 5 that composing two $C_2$'s, using the composition operator that is defined in the next section, yields a $C_3$ again.

(End of Example)

Example 3.3

Consider the $\text{And}$-element of Example 2.6 with input alphabet \{ $a$, $c$ \}, output alphabet \{ $b$, $p$ \}, and command $(a ; p ; r ; b ; c ; a ; c ; (p ; r ; b))$. It has independent alphabets \{ $a$, $b$, $c$ \} and \{ $p$, $r$ \}. In the trace structure that results after projection on \{ $a$, $b$, $c$ \}, having command $(a ; b ; c)$, the traces with an equal number of $a$'s, $b$'s, and $c$'s may be extended with input $a$. The same holds.
for traces with this property in the original trace structure. For alphabet \( \{ p, r \} \) it is even more clear that the requirements of independence are met.

(End of Example)

Example 3.4

The buffer of Example 2.8 has been constructed in such a way that data storage and data retrieval can be performed simultaneously. For data storage \( x_0 \) and \( x_1 \) are used and the request for new data is passed by \( a \). Outputs \( y_0 \) and \( y_1 \) return the stored value on request \( b \). Indeed, alphabets \( \{ x_0, x_1, a \} \) and \( \{ y_0, y_1, b \} \) are independent as can be seen from the state graph. After \( a \) either \( x_0 \) or \( x_1 \) is possible both in the original trace structure and in the trace structure with command \( \{ x_0 \mid x_1 ; a \} \), which results after projection on \( \{ x_0, x_1, a \} \). Projection on \( \{ y_0, y_1, b \} \) yields command \( \{ y_0 \mid y_1 ; b \} \). After \( y_0 \) or \( y_1 \), both in this and in the original trace structure \( b \) is possible.

(End of Example)

Example 3.5

The reason that the arbiter of Example 2.10 is sometimes preferred to the one of Example 2.9 is that the former's alphabet can be partitioned into independent alphabets. The input alphabet is \( \{ a, b, r \} \), the output alphabet \( \{ c, p, q \} \), and the command \( \{ (a ; p ; r), b \mid (b ; q ; r), a \} \). Independent alphabets are \( \{ a, b, c \} \) and \( \{ p, q, r \} \). Projection on \( \{ a, b, c \} \) yields command \( \{ a, b ; c \} \), from which we infer that the traces in this trace structure that have an equal number of \( a \)'s, \( b \)'s, and \( c \)'s may be extended with \( a \) and \( b \) in either order. The traces in the original trace structure have the same property. Projection on \( \{ p, q, r \} \) yields command \( \{ p \mid q \}; r \}, \) where \( p \) and \( q \) are outputs and \( r \) is an input. A trace in this trace structure may be extended with \( r \) if the sum of the numbers of \( p \)'s and \( q \)'s exceeds the number of \( r \)'s in that trace. The original trace structure has the same property.

(End of Example)

Example 3.6

The component of Example 2.12 has independent alphabets \( \{ c, e \} \) and \( \{ a, b, d, f \} \). Notice that, as opposed to inputs, outputs that disable one another may belong to different independent alphabets (to which the fact that \( \mathbb{C}_2 \) is not closed under composition can be attributed). The state graph that results after projection on \( \{ a, b, d, f \} \) is
Notice that this trace structure does not satisfy $R_4'$ anymore. Traces $f_b$ and $f_a b d b$ belong to the trace structure, whereas $f_b a b d b$ does not. Notice also that $\{a, f\}$ and $\{b, d\}$ are independent alphabets with respect to this trace structure. Projection on such an alphabet, however, yields a $C_4$ again.

(End of Example)

We conclude this section with a number of lemmata. We show that an input symbol of an independent alphabet $C$ may be shifted to the left over symbols of $C$ (and similarly output symbols to the right). Moreover, we prove that a $C_4$ projected on an independent alphabet is a $C_4$ again.

Lemma 3.0: For $T_a C_4$ with independent alphabet $C$, for traces $s$ and $t$, and for symbols $a \in a T$ and $b \in C \cap i T$
\[
s a b t \in t T \land s a t \in t T \implies s b a t \in t T
\]

Proof: If $a \in i T$ this lemma is a consequence of $R_3$. Therefore, assume
\[
a \in o T
\]

We prove the lemma by mathematical induction on the length of $t$.

Base: $t = \varepsilon$.
\[
s a b t \in t T \land s a t \in t T \implies s b a t \in t T
\]

$\Rightarrow \{ t T$ is prefix-closed and so is $t T \mid C$ according to Property 1.2 $\}$
\[
s a \in t T \land s \in t T \land s b \in t T \implies s a b \in t T
\]

$\Rightarrow \{ C$ is independent with respect to $T$ and $b \in C \cap i T$ $\}$
\[
s a \in t T \land s b \in t T
\]

$\Rightarrow \{ R_3''$, using $b \in i T$ and $a \in o T$ according to (0) $\}$
\[
sha \in t T
\]

$= \{ t = \varepsilon \}$
3.0. INDEPENDENT ALPHABETS

\[ s \in t \in T \]

Step: \( t \equiv t_{\varphi} \). Assume the left-hand side of the implication. Hence,

\[ s \in t \in T \text{ and } s \in t \rightarrow t \in T \rightarrow C \quad (1) \]

Then we derive

\[ \text{true} \]

\[ = \{ (1), \text{using } t = t_{\varphi} \text{ and the prefix-closedness of } t \in T \text{ and } t \in T \rightarrow C \} \]

\[ s \in t \in T \land s \in t \rightarrow t \in T \rightarrow C \]

\[ \Rightarrow \{ \text{induction hypothesis} \} \]

\[ s \in t \in T \quad (2) \]

Next, we distinguish three cases: (i) \( c \in o T \), (ii) \( c \in C \cap i T \), and (iii) \( c \in i T \setminus C \). We prove that \( s \in t \in T \) which yields the result desired, since \( t = t_{\varphi} \).

(i) \( c \in o T \)

\[ \text{true} \]

\[ = \{ (1) \text{ and } (2), \text{using } t = t_{\varphi} \} \]

\[ s \in t \in T \land s \in t \rightarrow t \in T \]

\[ \Rightarrow \{ \text{R}_T^{\varphi}, \text{since } b \in i T, \ c \in o T \text{ according to } (0), \text{ and } c \in o T \} \]

\[ s \in t \in T \]

(ii) \( c \in C \cap i T \)

\[ \text{true} \]

\[ = \{ (1) \text{ and } (2), \text{using } t = t_{\varphi} \} \]

\[ s \in t \rightarrow t \in T \rightarrow C \land s \in t \rightarrow t \in T \]

\[ \Rightarrow \{ C \text{ is independent with respect to } T \text{ and } c \in C \cap i T \} \]

\[ s \in t \in T \]

(iii) \( c \in i T \setminus C \)

\[ \text{true} \]

\[ = \{ (1), \text{using } t = t_{\varphi} \text{ and projection on } a T \setminus C, \text{ and } (2) \} \]

\[ s \in t \rightarrow t \rightarrow C \land s \in t \rightarrow t \rightarrow (a T \setminus C) \land s \in t \rightarrow t \]
\[
\begin{align*}
= & \{ \text{distribution of projection over concatenation, using } b \in C \} \\
\sigma_{bat} & \subseteq (T \setminus C) \subseteq T \setminus (T \setminus C) \wedge \sigma_{bat} \in tT \\
\Rightarrow & \{ a T \setminus C \text{ is independent with respect to } T, \text{ since } C \text{ is, and} \\
& c \in (a T \setminus C) \cap iT \} \\
\sigma_{bat} & \subseteq tT
\end{align*}
\]
(End of Proof)

Lemma 3.1: For \( T \) a \( C_q \) with independent alphabet \( C \), for traces \( s, t, \) and \( a \), and for symbol \( c \in C \cap iT \) such that \( t \wedge C = c \)

\[
\sigma_{at} \subseteq tT \Rightarrow \sigma_{at} \subseteq tT
\]

Proof: By mathematical induction on the length of \( t \).
Base: \( t = \epsilon \). Obvious.
Step: \( t = t_{ab} \). Hence, we have

\[
t_0 \wedge C = \epsilon \text{ and } b \notin C
\]

\[
\sigma_{at} \subseteq tT
\]

\[
= \{ t = t_{ab} \text{ and projection on } C \} \\
\sigma_{bat} & \subseteq tT \cap \sigma_{bat} \wedge C \subseteq tT \cap C
\]

\[
= \{ \text{distribution of projection over concatenation, using } b \notin C \text{ according to (0)} \} \\
\sigma_{bat} & \subseteq tT \wedge \sigma_{bat} \wedge C \subseteq tT \wedge C
\]

\[
\Rightarrow \{ \text{Lemma 3.0, since } a \in C \cap iT \} \\
\sigma_{bat} & \subseteq tT
\]

\[
\Rightarrow \left\{ \text{induction hypothesis, using (0)} \right\} \\
\sigma_{at} & \subseteq tT
\]

\[
\sigma_{at} \subseteq tT
\]
(End of Proof)

In a similar way, using that \( a T \setminus C \) is independent as well, we derive
Lemma 3.2: For $T$ a $C_4$ with independent alphabet $C$, for traces $s$, $t$, and $u$, and for symbol $a \in C \cap a T$ such that $t \mid C = \epsilon$

$$s a b u \in t T \implies s t a n u \in t T$$

Often we only want two symbols of the same type to be adjacent and we are not interested in the direction of the shifting. Therefore, we combine the last two lemmata, which yields

Lemma 3.3: For $T$ a $C_4$ with independent alphabet $C$, for traces $s$, $t$, and $u$, and for symbols $a \in C$ and $b \in C$ of the same type such that $t \mid C = \epsilon$

$$s a b u \in t T \implies s a b u \in t T \lor s t a n u \in t T$$

Lemma 3.4: For $T$ a $C_4$ with independent alphabet $C$, $T \mid C$ is a $C_4$ again.

Proof: We have to prove the 6 rules of the definition of $C_4$ to hold for $T \mid C$. $R_0$ through $R_4$ are fairly easy to prove, using for $R_2$ and $R_3$Lemma 3.3. We prove $R_4''$ and $R_4'''$ only.

$R_4'':$ for traces $s$ and $t$, and for symbols $a \in C$, $b \in C$, and $c \in C$ such that $b$ is of another type than $a$ and $c$

$$s a b c \in t T \mid C \land s h a t \in t T \mid C \implies s b a c \in t T \mid C$$

We distinguish two cases: (i) $b \in t T$, and (ii) $b \in o T$

(i) $b \in t T$

$$s a b c \in t T \mid C \land s h a t \in t T \mid C$$

$$= \{ \text{definition of projection, using that } t T \text{ is prefix-closed} \}$$

$$= \langle \exists s_0, s_1, s_2 : s_{0} a s_{1} b y e \in t T \land s h a t \in t T \mid C$$

$$C = s \land s_{1} \mid C = \epsilon \land s_{2} \mid C = t \rangle$$

$$= \{ \text{Lemma 2.1, since } C \text{ is independent with respect to } T, \text{ renaming} \}$$

$$= \langle \exists s_{0}, s_{1} : s_{0} a s_{1} c \in t T \land s h a t \in t T \mid C \land s_{0} \mid C = s \land s_{1} \mid C = t \rangle$$

$$= \{ \text{calculation, } T \text{ is prefix-closed, and distribution of projection over concatenation, using } a \in C \text{ and } b \in C \} \}$$
\((\exists s_0, s_1 : s \text{ abs} \cdot c \in t \cdot t \wedge s_0 \text{ abs} \cdot s_1 \in t \cdot t \wedge s_0 \text{ abs} \cdot s_1 \in t \cdot t \wedge s_0 \text{ abs} \cdot s_1 \in t \cdot t \wedge C \in t \cdot t \cdot C \equiv t) \equiv \{ \text{ Lemma } 3.0, \text{ since } s \in C \cap i \cdot T, \text{ renaming } \}\)
\[(\exists s_0, s_1 : s \text{ abs} \cdot c \in t \cdot t \wedge s_0 \text{ abs} \cdot s_1 \in t \cdot t \wedge C = s \equiv s_1 \cdot C = t) \equiv \{ \text{ projection, using } a \in C, b \in C, \text{ and } c \in C \}\]
\[s \text{ abs } \in t \cdot t \cdot C \]

(ii) \(b \in o \cdot T\)

\[s \text{ abs } \in t \cdot t \cdot C \cap \text{ abs } \in t \cdot t \cdot C \]
\[\equiv \{ \text{ definition of projection } \}\]
\[(\exists s_0, s_1, s_2 : s \text{ abs } \in t \cdot t \cdot C \wedge s_0 \text{ abs } \cdot s_2 \in t \cdot t \wedge s_0 \cdot C = s \wedge s_1 \cdot C = t) \equiv \{ \text{ Lemma } 3.2, \text{ since } C \text{ is independent with respect to } T, \text{ renaming } \}\]
\[(\exists s_0, s_1 : s \text{ abs } \cdot s_1 \in t \cdot t \cdot C \wedge C = s \wedge s_1 \cdot C = t) \equiv \{ \text{ calculus, } t \cdot t \cdot C \text{ is prefix-closed, and distribution of projection over concatenation, using } a \in C, b \in C, \text{ and } c \in C \}\]
\[(\exists s_0, s_1 : s_0 \text{ abs } \cdot s_1 \in t \cdot t \wedge s_0 \text{ abs } \cdot s_1 \in t \cdot t \wedge s_0 \text{ abs } \cdot s_1 \in t \cdot t \wedge s_0 \text{ abs } \cdot s_1 \in t \cdot t \wedge C \equiv t) \equiv \{ \text{ Lemma } 3.0, \text{ since } a \text{ is of another type than } b \text{ and, hence, } a \in C \cap i \cdot T, \text{ and } c \in C \}\]
\[(\exists s_0, s_1 : s_0 \text{ abs } \cdot s_1 \in t \cdot t \wedge s_0 \text{ abs } \cdot s_1 \in t \cdot t \wedge s_0 \text{ abs } \cdot s_1 \in t \cdot t \wedge C \equiv t) \equiv \{ \text{ projection, using } a \in C, b \in C, \text{ and } c \in C \}\]
\[s \text{ abs } \in t \cdot t \cdot C \]

\(R_5''':\) for trace \(s\), and for symbols \(a \in C\) and \(b \in C\) of different types
\[sa \in t \cdot t \cdot C \cap sb \in t \cdot t \cdot C \Rightarrow \text{ abs } \in t \cdot t \cdot C \]
3.1. Composition

Assuming that \( a \in C \cap \iota T \) and \( b \in C \cap \omicron T \) we derive

\[
\begin{align*}
\sigma a &\in tT[C \land \iota b \in tT[C \\
= \{ \text{definition of projection, using } tT \text{ is prefix-closed and } b \in C \} \\
(\exists \sigma_0 : a \in tT[C \land \sigma_0 \in tT \land \iota b \in tT \land \iota b \in tT[C = \iota]) \\
= \{ \text{calculus and distribution of projection over concatenation, using } a \in C \} \\
(\exists \sigma_0 : a \in tT[C \land \sigma_0 \in tT \land \iota b \in tT \land \iota b \in tT[C = \iota) \\
\Rightarrow \{ C \text{ is independent with respect to } T \text{ and } a \in C \cap \iota T \} \\
(\exists \sigma_0 : a \in tT \land \iota b \in tT \land \iota b \in tT[C = \iota) \\
\Rightarrow \{ \text{R}_C \} \\
(\exists \sigma_0 : a \in tT \land \iota b \in tT \land \iota b \in tT[C = \iota) \\
\Rightarrow \{ \text{projection, using } a \in C \text{ and } b \in C \} \\
\iota a b \in tT[C \land \iota b \in tT[C
\end{align*}
\]

Hence, \( \text{R}_C \) holds for \( a \in C \cap \omicron T \) and \( b \in C \cap \iota T \) as well.

(End of Proof)

3.1. Composition

Using independent alphabets we can state a number of conditions that guarantee absence of interference when composing two delay-insensitive trace structures. As we have argued in the preceding section and as will be proved in Lemma 3.5, blending two components by means of a set of common symbols that is independent with respect to both components guarantees absence of computation interference for the external communications with respect to the internal ones. In addition we impose two restrictions upon the internal communications. The first one is that each common symbol be an output symbol of the one and an input symbol of the other component. Second, we require the projections of both specifications on the set of common symbols to be equal. Formally this is captured in the following way:

**Definition 3.1:** Two \( C_i \)’s \( S \) and \( T \) are connectable if

1. \( aS \cap aT = (\omicron S \cap \iota T) \cup (\iota S \cap \omicron T) \)
2. \( aS \cap aT \) is independent with respect to both \( S \) and \( T \)
3. \( S[(aS \cap aT) = T[(aS \cap aT) \)

(End of Definition)
Notice that requirement 0) indeed states that each common symbol is an output symbol in the one and an input symbol in the other component, since \( 1S \cap oS = \emptyset \) (and \( 1T \cap oT = \emptyset \)). Requirement 2) in particular is a very stringent one. Even under these restrictions, however, it turns out to be quite delicate to prove absence of computation and transmission interference for the internal communications or to prove the various closure properties. Therefore, we confine ourselves in this monograph to Definition 3.1, indicating in Chapter 6 a number of ways to relax requirements 1) and 2).

As a preparation of the proof of absence of computation and transmission interference we conclude this section stating a few properties with regard to the input and output alphabets of the blend of two connectable \( C_s \)'s. They may be proved using that

\[
\begin{align*}
o(S \cdot T) &= o(S \cdot T) = (oS \cup oT) \setminus (aS \cap aT) \\
i(S \cdot T) &= i(S \cdot T) = (IS \cup IT) \setminus (aS \cap aT)
\end{align*}
\]

according to Chapter 1, and that the alphabet of a \( C_s \) consists of input and output symbols only according to \( R_0 \).

**Property 3.0:** For connectable \( C_s \)'s \( S \) and \( T \)

\begin{enumerate}
\item \( IS \cap IT = \emptyset = oS \cap oT \)
\item \( IS \setminus oT = IS \setminus aT \) and \( oS \setminus iT = oS \setminus aT \)
\item \( o(S \cdot T) = o(S \cdot T) = (oS \setminus iT) \cup (oT \setminus iS) \) and \( i(S \cdot T) = i(S \cdot T) = (IS \setminus oT) \cup (IT \setminus oS) \)
\item \( o(S \cdot T) \cup i(S \cdot T) = o(S \cdot T) \cup i(S \cdot T) = aS \setminus aT = a(S \cdot bT) \)
\end{enumerate}

(End of Property)

**Lemma 3.5:** For connectable \( C_s \)'s \( S \) and \( T \), for trace \( s \in t(S \cdot T) \), and for symbol \( a \in i(S \cdot T) \)

\[sa \in t(S \cdot T)\]

**Proof:** Without loss of generality we assume \( a \in aS \) and, hence, according to Property 3.0 (i), (ii), and (iii)

\[a \in IS \setminus aT\]

\[sa \in t(S \cdot T)\]
\[\Rightarrow \{ \text{projection on } aS = aT \}\]
\[sa [(aS = aT) \in t(S \cup T)] [\text{Definition of projection on } aS = aT]\]
\[\Rightarrow \{ \text{One of the premises is } s \in t(S \cup T). \text{Property 1.8, using the definition of}\]
\[\text{blending}\}\]
\[s \in t(S \cup T) \land sa [(aS = aT) [(aS \setminus aT) \in tS [(aS \setminus aT)]]\]
\[\Rightarrow \{ \text{definition of weaving and distribution of projection over concatenation,}\]
\[\text{using Property 1.1 and } aS \setminus aT \subseteq aS = aT \text{ and } aS \setminus aT \subseteq aS \}\]
\[s [(aS \setminus aT) \in tT \land \{ t[(aS \setminus aT)]\} \in tS [(aS \setminus aT)]\]
\[\Rightarrow \{ aS \setminus aT \text{ is independent with respect to } S, \text{since } S \text{ and } T \text{ are connectable, and } a \in (aS \setminus aT) \cap iS \text{ according to (0)}\}\]
\[\{ t[(aS \setminus aT)] \in tS \land \{ aT \in tT\}\]
\[\Rightarrow \{ \text{distribution of projection over concatenation, using (0)}\}\]
\[sa [(aS \subseteq tS) \land sa [aT \subseteq tT]\]
\[\Rightarrow \{ \text{definition of weaving, using } s \in (aS \cup aT)^* \text{ and } a \in aS \cup aT\}\]
\[sa \in t(S \cup T)\]

(End of Proof)
4

Internal communications and external specification

The main issue of this chapter is to show absence of transmission and computation interference under composition of connectable C-4's. Since interference is a physical notion for mechanisms that send and receive signals, we begin this chapter with the introduction of a mechanistic appreciation of composition. In the last section it is argued that the blend is an operator for the specification of the composite that is in accordance with this mechanistic appreciation.

4.0. An informal mechanistic appreciation

We consider a mechanism and its environment that communicate with one another by sending and receiving signals. There are two types of signals: from the environment to the mechanism, the so-called inputs, and from the mechanism to the environment, which we call outputs. We assume that signals are conveyed via (finitely many) wires. With each wire we associate a symbol. A signal via a wire is denoted by its associated symbol.

A trace structure is viewed as the specification of such a mechanism-environment pair. Each symbol of the trace structure's alphabet corresponds to one wire. The alphabet is partitioned into an input and an output alphabet.

A trace is conceived as a sequence of events. Due to the concurrency of signals and the dependency of observations upon the position, there might not exist a unique sequence of events that describes the history of a mechanism-environment pair in operation. This history is rather described, at any time during operation, by a set, or equivalence class, of sequences of events. Traces that differ from one another because of the concurrency of symbols belong to the same equivalence class. Yet we associate, at any time during operation, one single trace, being a
sequence of events, with the operation of a mechanism-environment pair. This
trace is called the trace thus far generated. Since the discussion in the sequel
relates to an arbitrary trace thus far generated it pertains, in fact, to the
equivalence class of sequences of events.

Initially, the trace thus far generated is \( \varepsilon \). The operation of the mechanism-
environment pair corresponds to the generation of symbols. Each signal that the
mechanism and environment communicate with one another can be viewed as
the extension of the trace thus far generated with the symbol that is associated
with that signal. Notice that only those extensions are allowed that yield a trace
that belongs to the trace structure again.

We say that output symbols in the trace thus far generated have been sent
and input symbols have been received by the mechanism. Whenever more con-
venient, we say that these symbols have been sent or received by the trace thus
far generated instead of by the mechanism.

Under composition of two mechanism, wires to which the same symbol

\begin{itemize}
\item \textbf{equivalent:} a wire that conveys input signals to the one mecha-
nism should convey output signals from the other one. Accordingly, under com-
position of two trace structures, a common symbol is an input symbol of the one

\item \textbf{composable:} and an output symbol of the other trace structure. Composition can be viewed as

\item \textbf{completable:} replacing (a part of) one mechanism's environment by the other mechanism-
environment pair.

\item \textbf{composing:} We assume the so-called causality rule for mechanisms, i.e. no input signal
can be received before the corresponding output signal has been sent. For the
mechanistic appreciation of composition this means the following. At any instant,
there are two traces thus far generated, one for each of the mechanism-
environment pairs. Each trace may be extended with a symbol in the way
described above, under the additional restriction that for each common symbol
the number of times it has been received by the one trace does not exceed the
number of times it has been sent by the other one.

\item \textbf{composing:} A symbol sent by the one trace that has not been received by the other one is
said to be on its way. Any two traces that can be brought about observing the
restrictions above are called composable.

\item \textbf{composing:} Absence of transmission and computation interference can be expressed in
terms of composable traces. There is absence of transmission interference if we
have for all pairs of composable traces: the number of occurrences of a common
symbol sent by the one trace exceeds the number of occurrences of that symbol
received by the other trace by at most one. There is absence of computation
interference if we have for all pairs of composable traces: a symbol on its way
from one trace to the other can be received by the latter, i.e. the extension of the
latter trace with this symbol belongs to the trace structure of the corresponding
mechanism.

\item \textbf{composing:} We prove in the next sections that there is absence of computation and
transmission interference under composition of two connectable \( C_4 \)'s. Therefore,
we believe that the formal properties of delay-insensitivity and connectability provide a model that can be usefully applied to the problem of composing physical circuits and deriving the specification for the resulting circuit from the specifications of the composing circuits.

4.1. Formalization of the mechanistic appreciation

In this section we formalize the mechanistic appreciation as introduced above and the proof obligations for showing absence of transmission and computation interference.

Definition 4.0 : For connectable trace structures $T$ and $U$, the composability of traces $t \in T$ and $u \in U$, denoted by $c(t, u)$, is defined by

$$t = \epsilon \land u = \epsilon \lor$$

$$(\exists \alpha, t_0 : t = t_0 \land c(\alpha, u) \land (a \in o \ U \Rightarrow \#_a t > \#_a t_0)) \lor$$

$$(\exists b, u_0 : u = u_0 \land c(t, u_0) \land (b \in o \ T \Rightarrow \#_b t > \#_b u_0))$$

(End of Definition)

Notice that $c(t, u) = c(u, t)$. Notice also that $a \in o \ U$ and $t = t_0 \alpha$ implies $a \in a \ T \cap a \ U$ and, hence, on account of the definition of connectability, $a \in T$.

To cope with the various appearances of the arguments of $c$ we state the following properties, which can readily be derived from the definition of $c$. When referring to the definition of $c$, one of the following properties may be meant.

Property 4.0 :

(i) $c(\alpha, u, b) = c(t, u, b) \land (a \in o \ U \Rightarrow \#_a u > \#_a t) \lor$

$c(\alpha, u, b) \land (b \in o \ T \Rightarrow \#_b u > \#_b \alpha)$

(ii) $c(\alpha, u, b) = c(t, u, b) \land (a \in o \ U \Rightarrow \#_a u > \#_a t) \lor$

$$(\exists b, u_0 : u = u_0 \land c(t, u_0) \land$$

$$(b \in o \ T \Rightarrow \#_b t > \#_b u_0))$$

(iii) $c(t, \epsilon) = (t \cap o \ U = \epsilon)$

(End of Property)

The two theorems that we have to prove are
4.1. Formalization of the Mechanistic Appreciation

Theorem 4.0: (Absence of transmission interference) For connectable Cₜ's T and U, for composable traces \( t \in T \) and \( u \in U \), and for symbol \( a \in o T \cap i U \), \( \#ₜ t - \#ₜ u \leq 1 \).

Theorem 4.1: (Absence of computation interference) For connectable Cₜ's T and U, for composable traces \( t \in T \) and \( u \in U \), and for symbol \( a \in o T \cap i U \) such that \( \#ₜ t > \#ₜ u : a \in t U \).

These two theorems are proved in the next section. We conclude this section with a few lemmata on composable traces.

Lemma 4.0: For connectable Cₜ's T and U, for traces \( t \) and \( u \), and for symbol \( a \) such that \( as \in T \) and \( u \in t U \):

\[ c(ts, u) \land a \notin t U \Rightarrow c(t, u) \]

Proof: By mathematical induction on the length of \( u \).

Base: \( u = \varepsilon \).

\[ c(ts, u) \land a \notin t U \]

\[ \Rightarrow \{ u = \varepsilon \} \]

\[ c(ts, \varepsilon) \]

\[ = \{ \text{definition of } c \} \]

\[ c(t, \varepsilon) \]

\[ = \{ u = \varepsilon \} \]

\[ c(t, u) \]

Step: \( u = u_0 b \). Now we derive

\[ c(ts, u) \land a \notin t U \]

\[ = \{ u = u_0 b \} \]

\[ c(ts, u_0 b) \land a \notin t U \]

\[ = \{ \text{definition of } c \text{ and calculus} \} \]

\[ c(t, u_0 b) \land (a \in o U \Rightarrow \#ₜ u_0 > \#ₜ t) \land a \notin t U \lor \]

\[ c(ts, u_0) \land (b \in o T \Rightarrow \#ₜ u_0 > \#ₜ u) \land a \notin t U \]

\[ = \{ \text{calculus, using } u = u_0 b \text{, and the induction hypothesis} \} \]

\[ c(t, u) \lor c(t, u_0) \land (b \in o T \Rightarrow \#ₜ u_0 > \#ₜ u) \land a \notin t U \]
\[ \Rightarrow ( \text{ calculus, using that } b \in a \text{ and } b \in o T \text{ implies, by the connectability of } \\
T \text{ and } U, b \in 1U ) \]
\[
c,(t,u) \lor c,(t,u_0) \land b \in o T \lor c,(t,u_0) \land \#_a u_0 > \#_a u_0 \land a \neq b
\]
\[ \Rightarrow ( \text{ definition of } c, \text{ using } u = u_0b, \text{ and calculus } ) \]
\[
c,(t,u) \lor c,(t,u_0) \land \#_a t > \#_a u_0
\]
\[ \Rightarrow ( \text{ definition of } c, \text{ using } u = u_0b ) \]
\[
c,(t,u)
\]

(End of Proof)

**Lemma 4.1**: For connectable C₄'s T and U, for traces t and u, and for symbol a such that ta \in t T and u \in t U

\[
c,(ta,u) \land \#_a ta > \#_a u \Rightarrow c,(t,u)
\]

**Proof**: By mathematical induction on the length of u.

**Base**: u = ε.

\[
c,(ta,u) \land \#_a ta > \#_a u
\]
\[ = \{ u = \epsilon \}
\]
\[
c,(ta,\epsilon)
\]
\[ = \{ u = \epsilon \}
\]
\[
c,(t,\epsilon)
\]

**Step**: u = u₀b. Now we derive

\[
c,(ta,u) \land \#_a ta > \#_a u
\]
\[ = \{ u = u₀b \}
\]
\[
c,(ta,u₀b) \land \#_a ta > \#_a u₀b
\]
\[ = \{ \text{ definition of } c \text{ and calculus } \}
\]
\[
c,(t,u₀b) \land (a \in o U \Rightarrow \#_a u₀b > \#_a u₀) \land \#_a ta > \#_a u₀b
\]
\[
c,(ta,u₀b) \land (b \in o T \Rightarrow \#_b ta > \#_b u₀) \land \#_a ta > \#_a u₀b
\]
\[ \Rightarrow ( \text{ calculus, using } u = u₀b ) \]
\[
c,(t,u) \lor c,(t,u₀b) \land (b \in o T \Rightarrow \#_b ta > \#_b u₀) \land \#_a ta > \#_a u₀b \land a = b \lor c,(ta,u₀b) \land (b \in o T \Rightarrow \#_b ta > \#_b u₀) \land \#_a ta > \#_a u₀b \land a \neq b
\]
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\[
\Rightarrow \{ \text{calculus} \}
\]
\[
c(t, u) \lor c(u, u_0) \land \#_a t \supset \#_a u_0 \land a = b \lor
\]
\[
c(t, u_0) \land (b \in T \Rightarrow \#_a t \supset \#_a u_0) \land \#_a T \supset \#_a u_0
\]
\[
\Rightarrow \{ \text{induction hypothesis and calculus} \}
\]
\[
c(t, u) \lor c(t, u_0) \land \#_a t \supset \#_a u_0 \lor c(t, u_0) \land (b \in T \Rightarrow \#_a t \supset \#_a u_0)
\]
\[
\Rightarrow \{ \text{definition of } c, \text{ using } u = u_0 \phi \}
\]
\[
c(t, u)
\]
(End of Proof)

\textbf{Lemma 4.2:} For connectable C_\text{t}'s T and U, for traces t \in T and u \in U, and for symbol a \in a T \cap a U
\[
c(t, u) \land \#_a t \supset \#_a u \Rightarrow a \in o T \cap i U
\]

\textbf{Proof:} By mathematical induction on 1t + 1u.

\textbf{Base:} 1t + 1u = 0. Then \#_a t = \#_a u.

\textbf{Step:} 1t + 1u = k, for some k, k \geq 1. Now we derive
\[
c(t, u) \land \#_a t \supset \#_a u
\]
\[
\Rightarrow \{ \text{definition of } c, \text{ using } - (t = c \land u = c) \text{ since } k \geq 1. \text{ Calculus} \}
\]
\[
(\exists b, t_0 : t = t_0 b \land c(t_0, u) \land (b \in o U \Rightarrow \#_a u \supset \#_a t_0) \land \#_a t_0 \supset \#_a u) \lor
\]
\[
(\exists b, t_0 : u = u_0 b \land c(t, u_0) \land (b \in o T \Rightarrow \#_a t \supset \#_a u_0) \land \#_a T \supset \#_a u_0 b)
\]
\[
\Rightarrow \{ \text{induction hypothesis applied to the second disjunct. Calculus} \}
\]
\[
(\exists b, t_0 : b = a \land (b \in o U \Rightarrow \#_a u \supset \#_a t_0) \land \#_a t_0 \supset \#_a u) \lor
\]
\[
(\exists b, t_0 : b \neq a \land c(t_0, u) \land \#_a t_0 \supset \#_a u) \lor a \in o T \cap i U
\]
\[
\Rightarrow \{ \text{induction hypothesis applied to the second disjunct. Calculus} \}
\]
\[
(\exists b, t_0 : (a \in o U \Rightarrow \#_a u \supset \#_a t_0) \land \#_a t_0 \supset \#_a u) \lor a \in o T \cap i U
\]
\[
\Rightarrow \{ \text{calculus, using } a \in a T \cap a U \text{ and the connectability of } T \text{ and } U \text{ on account of which } a \in o U = a \in o T \cap i U \}
\]
\[
a \in o T \cap i U
\]
(End of Proof)

From Lemma 4.2 we infer the following corollary, using the symmetry of c and (o T \cap i U) \cap (o U \cap i T) = \varnothing.

\[
\]
Corollary 4.0: For connectable C₄'s T and U, for traces \( t \in T \) and \( u \in U \), and for symbol \( a \in a T \cap a U \)
\[
e(1, u) \land a \in a T \cap a U \Rightarrow \#_a t \geq \#_a u
\]

4.2. Absence of transmission and computation interference

(This section may be skipped on first reading.) In this section we prove the absence of transmission and computation interference. To that end we consider, for connectable trace structures T and U and for composable traces \( t \in T \) and \( u \in U \), symbols on their way from one trace to the other. Rather than considering these symbols individually, we consider the set of sequences of symbols, called traces again, consisting of the symbols on their way in one direction.

Definition 4.1: For connectable trace structures T and U, and for composable traces \( t \in T \) and \( u \in U \) we define \( \text{from}(t, u) \) as

\[
\{ x : x \in (o T \cap i U)^* \land (\forall a : a \in o T \cap i U : \#_a x = \#_a t - \#_a u) : x \}
\]
(End of Definition)

Consequently, \( \text{from}(t, u) \) is the set of traces that are a permutation of all symbols sent by \( t \) and not received by \( u \). Since \( \#_a t \geq \#_a u \) for \( a \in o T \cap i U \) on account of Corollary 4.0, \( \text{from}(t, u) \) is non-empty. Since the lengths of the traces in \( \text{from}(t, u) \) are equal, we define \( l(\text{from}(t, u)) \) as the length of the traces in \( \text{from}(t, u) \), which is the number of symbols on their way from \( t \) to \( u \).

The total number of symbols on their way between \( t \) and \( u \) is called the number of mismatches and is denoted by \( \text{mm}(t, u) \).

Definition 4.2: For connectable trace structures T and U, and for composable traces \( t \in T \) and \( u \in U \)
\[
\text{mm}(t, u) = l(\text{from}(t, u)) + l(\text{from}(u, t))
\]
(End of Definition)

We shall frequently use the following properties of from and mm. Proofs are omitted but can be derived using the definitions and Lemmata 4.0 and 4.2.
Property 4.1: For connectable trace structures \( T \) and \( U \), and for composable traces \( t \in tT \) and \( u \in tU \)

(i) \( u_0 \in \text{from}(t, u) \land u_0 \in tU \Rightarrow \epsilon \in \text{from}(t, u_0) \)

(ii) \( u_0 \in \text{from}(t, u) \land u_0 \in tU \Rightarrow \text{from}(t, u_0) = \{ \epsilon \} \)

(iii) \( u = u_0u_1 \land u_1 \in o T^* \Rightarrow \text{mm}(t, u_0) = \text{mm}(t, u_0u_1) + 1u_1 \)

(iv) \( u = u_0u_1 \land u_1 \in o T^* \land u_2 \in \text{from}(t, u_0u_1) \Rightarrow u_1u_2 \in \text{from}(t, u_0) \)

(v) \( u = u_0u_1 \land u_1 \in o T^* \Rightarrow \text{from}(u_0u_1, t) = \text{from}(u_0, t) \)

(End of Property)

In order to prove Theorems 4.0 and 4.1, the absence of transmission and computation interference respectively, we prove the following lemma.

Lemma 4.3: For connectable trace structures \( T \) and \( U \) such that \( aT = aU \), and for composable traces \( t \in tT \) and \( u \in tU \)

\((\forall u_0 : u_0 \in \text{from}(t, u) : u_0 \in tU)\)

Theorems 4.0 and 4.1 are derived from this lemma in the following way. Let \( T \) and \( U \) be connectable \( C_4 \)'s and let \( t \in tT \) and \( u \in tU \) be composable traces. Since \( aT \cap aU \) is independent with respect to both \( T \) and \( U \), \( T[aT \cap aU] \) and \( U[aT \cap aU] \) are \( C_4 \)'s as well according to Lemma 3.4. Moreover, their alphabets are equal, viz. \( aT \cap aU \), and they are connectable as follows from the definition of connectability.

From the definition of compositability it can be seen that the strings of common symbols in \( t \) and \( u \) determine both the compositability of \( t \) and \( u \) and the symbols on their way from \( t \) to \( u \). Hence, from the compositability of \( t \) and \( u \) with respect to \( T \) and \( U \) we infer the compositability of \( t[aT \cap aU] \) and \( u[aT \cap aU] \) with respect to \( T[aT \cap aU] \) and \( U[aT \cap aU] \). Let \( u_0 \) be a string of all symbols on their way from \( t \) to \( u \). Then \( u_0 \) is a string of all symbols on their way from \( t[aT \cap aU] \) to \( u[aT \cap aU] \) as well. This implies that \((u[aT \cap aU])u_0 \in tU[aT \cap aU]\) according to Lemma 4.3. The symbols of \( u_0 \) are input symbols to \( U \) and belong to \( aT \cap aU \). Since \( aT \cap aU \) is independent with respect to \( U \) and since \( u \in tU \) we conclude, by applying Definition 3.0 a number of times, that \( u_0 \in tU \).
Notice that \( u_0 \) is an arbitrary permutation of the symbols on their way from \( T \) to \( U \), i.e. of symbols \( a = o T \cap 1 U \) with \( \#_a t > \#_a u \). First, \( u_0 \in t U \) then implies that each symbol occurs at most once in \( u_0 \), since, on account of \( R_2 \), adjacent symbols are distinct. Hence, we have proved absence of transmission interference. Second, again since \( u_0 \) is an arbitrary permutation of symbols on their way and, hence, may start with any symbol on its way from \( t \) to \( u \), it implies absence of computation interference, since \( t U \) is prefix-closed.

Lemma 4.3 is proved by mathematical induction. In order to reduce the length of the proof we first prove two additional lemmata, in which we assume the induction hypothesis for Lemma 4.3. Let, for the remainder of this section \( T \) and \( U \) be connectable \( C_4 \)s such that \( a T = a U \). Consequently, \( i T = o U \), \( o T = i U \), and \( t T = t U \).

**Lemma 4.4**: Given integer \( k \) and given that all composable traces \( t \in t T \) and \( u \in t U \) with \( 1t + 1u + \text{mm}(t, u) \leq k \) satisfy

\[
(\forall u_0 : u_0 \in \text{from}(t, u) : u_0 \in t U) \land (\forall t_0 : t_0 \in \text{from}(u, t) : t_0 \in t T)
\]

Then for traces \( s, t, u, v, \) and \( w \), and for symbol \( a \in o T \) such that \( t \in a U^v, \)
\( v \in o T^w \), \( satw \in t T \), and \( uw \in t U \)

\[
e(\text{sat}, \text{uw}) \land 1(\text{sat}) + 1(\text{uw}) + \text{mm}(\text{sat}, \text{uw}) \leq k
\]

\[
\Rightarrow \text{satw} \in t T \land \text{uw} \in t U \land e(\text{satw}, \text{uw})
\]

**Proof**: By mathematical induction on the length of \( w \).

**Base**: \( w = \epsilon \). We assume the left-hand side of the implication, hence,

\[
e(\text{sat}, \text{uw}) \land 1(\text{sat}) + 1(\text{uw}) + \text{mm}(\text{sat}, \text{uw}) \leq k
\]

(0)

Let \( t_0 \) and \( u_0 \) be such that

\[
t_0 \in \text{from}(\text{uw}, \text{sat}) \land u_0 \in \text{from}(\text{sat}, \text{uw})
\]

(1)

Since \( t T \) is prefix-closed, and since \( a \) and the symbols of \( v \) are of the same type, which makes \( R_3 \) applicable, we have

\[
sa \in t T \land uw \in t U
\]

(2)

Now we derive

\[
\text{true}
\]

\[
= \{ (0) \text{ and } (1) \}
\]

\[
e(\text{sat}, \text{uw}) \land 1(\text{sat}) + 1(\text{uw}) + \text{mm}(\text{sat}, \text{uw}) \leq k \land
\]
4.2. Absence of Transmission and Computation Interference

\[ t_0 \in \text{from}(\text{waw}, \text{sat}) \land u_0 \in \text{from}(\text{sat}, \text{waw}) \]

\[ \Rightarrow \{ \text{Lemma 4.0, Property 4.1 (iii), (iv), and (v), using } t \in o U^+ \} \]

\[ c(sa, waw) \land 1(sa) + 1(waw) + \text{mm}(sa, u) \leq k \land u_0 \in \text{from}(\text{waw}, sa) \land u_0 \in \text{from}(sa, waw) \]

\[ \Rightarrow \{ \text{Lemma 4.0, Corollary 4.0, and Property 4.1 (iii), (iv), and (v), using } \]

\[ \alpha \in o T^+ \}

\[ c(sa, u) \land \#sa > \#u \land 1(sa) + 1u + \text{mm}(sa, u) \leq k \land \]

\[ t_0 \in \text{from}(u, sa) \land u_0 \in \text{from}(sa, u) \]

\[ \Rightarrow \{ \text{Lemma 4.1, definition of mm and from, using } \alpha \in o T^+ \}

\[ c(s, u) \land 1s + 1u + \text{mm}(s, u) \leq k - 2 \land t_0 \in \text{from}(u, s) \land u_0 \in \text{from}(s, u) \]

\[ \Rightarrow \{ \text{premise} \}

\[ stt_0 \in t T \land uaw_0 \in t U \land c(s, u) \land t_0 \in \text{from}(u, s) \land u_0 \in \text{from}(s, u) \]

\[ \Rightarrow \{ \text{Property 4.1 (i) and (v), using } t_0 \in o U^+ \text{ and } u_0 \in o T^+ \}

\[ stt_0 \in t T \land uaw_0 \in t U \land c(stt_0, uaw_0) \]

\[ \Rightarrow \{ t T \text{ and } t U \text{ are prefix-closed, Lemma 4.0, using } t_0 \in o U^+ \text{ and } u_0 \in o T^+ \}

\[ st \in t T \land w \in t U \land c(st, w) \]

\[ \Rightarrow \{ \text{Lemma 2.2, using (2) and the fact that } a \text{ and the symbols of } t \text{ are of different types} \}

\[ sta \in t T \land wa \in t U \land c(st, w) \]

\[ \Rightarrow \{ \text{definition of } c, \text{ using } \#st \gg \#wa \text{ according to Corollary 4.0} \}

\[ sta \in t T \land wa \in t U \land c(sta, wa) \]

\[ \Rightarrow \{ w = s \}

\[ staw \in t T \land waw \in t U \land c(staw, waw) \]

**Step:** \( w = u_{w_0} \). Assuming the left-hand side of the implication again, we have

\[ c(satw_{w_0}, uaw_{w_0}) \text{ and } \]

\[ l(satw_{w_0}) + 1(uaw_{w_0}) + \text{mm}(satw_{w_0}, uaw_{w_0}) \leq k \] (3)

Now we derive

\[ \text{true} \]

\[ \Rightarrow \{ (3) \}

\[ satw_{w_0} \in t T \land uaw_{w_0} \in t U \land c(satu_{w_0}, uaw_{w_0}) \land l(satu_{w_0}) + 1(uaw_{w_0}) + \text{mm}(satw_{w_0}, uaw_{w_0}) \leq k \]

\[ \Rightarrow \{ \text{Lemma 4.0 and 4.1, Corollary 4.0, and the definition of mm} \}

\[ satu_{w_0} \in t T \land uaw_{w_0} \in t U \land c(satu_{w_0}, uaw_{w_0}) \land \]
1(satv₀) + 1(wauv₀) + mm(satv₀, wauv₀) ≤ k − 2
⇒ \{ induction hypothesis and the definition of mm. Moreover, a and the symbols of \( v \) are of the same type, which makes R₃ applicable \}

\begin{align*}
\text{satv₀} & \in T \land \text{stauv₀} \in t \land \text{wauv₀} \in U \land c(stauv₀, wauv₀) \land \\
1(satv₀) + 1(wauv₀) + mm(satv₀, wauv₀) & \leq k - 2
\end{align*}

We distinguish two cases: (i) \( c \in o \ T \) and (ii) \( c \in o \ U \)

(i) \( c \in o \ T \)

true

\begin{align*}
\Rightarrow & \{ (4), applying Lemma 2.6, using that the symbols of \( t \) are of another type than \( a \) and \( c \); calculus \}
\text{stauv₀} & \in T \land \text{wauv₀} \in U \land c(stauv₀, wauv₀) \\
\Rightarrow & \{ definition of \( c \), using \#_v stauv₀ \geq \#_v wauv₀ according to Corollary 4.0 \}
\text{stauv₀} & \in T \land \text{wauv₀} \in U \land c(stauv₀, wauv₀) \\
\Rightarrow & \{ w \leq a \}
\text{stau} & \in T \land \text{wau} \in U \land c(stau, wau)
\end{align*}

(ii) \( c \in o \ U \)

true

\begin{align*}
\Rightarrow & \{ (4) and calculus \}
\text{stauv₀} & \in T \land \text{wauv₀} \in U \land c(stauv₀, wauv₀) \\
1(satv₀) + 1(wauv₀) + mm(satv₀, wauv₀) & \leq k - 2 \\
\Rightarrow & \{ definition of \( c \) and mm, using \( c \in o \ U \) and \#_v wauv₀ \geq \#_v stauv₀ according to Corollary 4.0 \}
\text{stauv₀} & \in T \land \text{wauv₀} \in U \land c(stauv₀, wauv₀) \land \#_v wauv₀ \geq \#_v stauv₀ \land \\
1(satv₀) + 1(wauv₀) + mm(satv₀, wauv₀) & \leq k \\
\Rightarrow & \{ premise, using that \( T \) is prefix-closed and that there is a trace in \from(\text{wauv₀}, \text{stauv₀}) \) that begins with \( c \) \}
\text{stauv₀} & \in T \land \text{wauv₀} \in U \land c(stauv₀, wauv₀) \land \#_v wauv₀ \geq \#_v stauv₀ \\
\Rightarrow & \{ definition of \( c \), using \( c \in o \ U \) \}
\text{stauv₀} & \in T \land \text{wauv₀} \in U \land c(stauv₀, wauv₀) \\
\Rightarrow & \{ w = w \}
\text{stau} & \in T \land \text{wau} \in U \land c(stau, wau)
4.2. ABSENCE OF TRANSMISSION AND COMPUTATION INTERFERENCE

(End of Proof)

Lemma 4.5: Given integer \( k \) and given that all composable traces \( t \in T \) and \( u \in U \) with \( 1t + 1u + \text{mm}(t, u) \leq k \) satisfy

\[
(\forall u_0 : u_0 \in \text{from}(t, u) : u_0 \in T) \land (\forall u_0 : u_0 \in \text{from}(u, t) : u_0 \in T)
\]

Then for composable traces \( t \in T \) and \( u \in U \) with \( 1t + 1u = k - 1 \) and \( \text{mm}(t, u) = 0 \)

\[
(\forall e : e \in o T : \text{c}(e, t) \in t U \Rightarrow \text{c}(e, u) \in u U) \land (\forall e : e \in o U : \text{c}(e, u) \in u U \Rightarrow \text{c}(e, t) \in t U)
\]

Proof: We observe, since \( a T = a U \), that the lengths of composable traces \( t \in T \) and \( u \in U \) with \( \text{mm}(t, u) = 0 \) are equal. Moreover, if these traces are non-empty, at least one of them contains a symbol that is an output symbol for the trace structure to which that trace belongs.

We prove the theorem by mathematical induction on the length of the longest common suffix of \( t \) and \( u \).

Base: The length of the longest common suffix of \( t \) and \( u \) equals \( 1t \) and \( 1u \). Then \( t = u \) and the lemma holds, since \( t T = t U \), due to the connectability of \( T \) and \( U \).

Step:

\[
t = t_{\delta e} \quad \text{and} \quad u = u_{\delta e} \quad \text{with} \quad \text{c}(t, u), 1t + 1u = k - 1, \quad \text{and} \quad \text{mm}(t, u) = 0
\]

(0)

Traces \( t_0 \) and \( u_0 \) are non-empty and do not end in the same symbol. Notice that we assume the lemma to hold for composable traces with a longest common suffix that is longer than \( u \). Applying Lemmata 4.0 and 4.1, using the definition of \( \text{from} \), we derive from (0) and the compositability of \( t \) and \( u \)

\[
\text{c}(t_0, u_0) \quad \text{and} \quad \text{mm}(t_0, u_0) = 0
\]

(1)

We define \( r, t_2, s, \) and \( v \) in the following way.

\[
t_2 = t_r \quad \text{and} \quad u_0 = u_0 \quad \text{with} \quad t_2 \in o U^* \quad \text{and} \quad u \in o T^*
\]

(2)

\[
r \quad \text{and} \quad v \quad \text{do not end in a symbol of} \quad o U \quad \text{and} \quad o T \quad \text{respectively}
\]

(3)

Moreover we have, according to the observation made at the beginning of this proof,

\[
1r + 1s > 0
\]

(4)
Now we derive
\[ \text{true} \]
\[ c(r_2, o) \land i_2 \in o \quad U^* \land v \in o \quad T^* \]
\[ \Rightarrow \{ \text{Lemma 4.0} \} \]
\[ c(r, s) \]
\[ \Rightarrow \{ \text{definition of } c, \text{ using } (4) \} \]
\[ (3t_1, a : t = t_1 a \land c(t_1, t) \land (a \in o \ U \Rightarrow \# a \geq \# a t_1)) \lor \]
\[ (3t_1, a : t = u_1 a \land c(r, s_1) \land (a \in o \ T \Rightarrow \# a \geq \# a u_1)) \]

Without loss of generality we may, due to the symmetric formulation of this lemma, assume the first disjunct to hold and, hence, \( i_1 \) and \( a \) to be defined. Hence, we have
\[ r = t_1 a \]
\[ c(t_1, t) \]
\[ a \in o \quad T \]
\[ c(t_1, t, r) \]

(8) follows from (3) and (6), and (9) follows from (5) and (6). According to Corollary 4.0 we infer from (7) and (8) that \( \# a t_1 \geq \# a r \). Hence, from (6), (1), and (2) we conclude that \( v \) contains symbol \( a \). Consequently, using (2), we may assume traces \( u_1 \) and \( u_2 \) to be defined such that
\[ w = u_1 a u_2 \text{ and } u_2 \in o \quad T^* \]

Combining (0) through (10), we infer
\[ t = t_1 a t_2 w \text{ and } u = u_1 a u_2 w \]
\[ a \in o \quad T \]
\[ t_2 \in o \quad U^* \text{ and } u_2 \in o \quad T^* \]
\[ c(t_1 a t_2 w, u_1 a u_2 w) \]
\[ \text{min}(t_1 a t_2 w, u_1 a u_2 w) = k - 1 \text{ and } \text{min}(t_1 a t_2 w, u_1 a u_2 w) = 0 \]

which allows us to apply Lemma 4.4 and derive
\[ t_1 a t_2 w \in t \quad T \text{ and } u_1 a u_2 w \in t \quad U \text{ and } c(t_1 a t_2 w, u_1 a u_2 w) \]
4.2. ABSENCE OF TRANSMISSION AND COMPUTATION INTERFERENCE

Now we may apply the induction hypothesis, since the longest common suffix of $t_1a_2aw$ and $u_1u_2aw$ is at least $aw$, which is longer than $w$, and, moreover, $1(1)_{t_1a_2aw} + 1(u_1u_2aw) = k - 1$ and $\min(1(1)_{t_1a_2aw}, u_1u_2aw) = 0$, due to (15).

Hence, we have

\[
\begin{align*}
(\forall \varepsilon : \varepsilon \in o \ T : t_1a_2aw \varepsilon \in t \ T \Rightarrow u_1u_2aw \varepsilon \in t \ U) & \quad (17) \\
(\forall \varepsilon : \varepsilon \in o \ U : u_1u_2aw \varepsilon \in t \ U \Rightarrow t_1a_2aw \varepsilon \in t \ T) & \quad (18)
\end{align*}
\]

from which we derive for any $\varepsilon \in o \ T$

\[
\begin{align*}
w & \in t \ T \\
= & \quad \{ (11) \text{ and (16) } \}
\end{align*}
\]

\[
\begin{align*}
t_1a_2aw & \in t \ T \land t_1a_2aw \in t \ T \\
\Rightarrow & \quad \{ \text{ Lemma 2.6, since } t_2 \in o \ U^* \text{ according to (13), } a \in o \ T \text{ according to (12), and } \varepsilon \in o \ T \}
\end{align*}
\]

\[
\begin{align*}
t_1a_2aw & \in t \ T \\
= & \quad \{ (17) \}
\end{align*}
\]

\[
\begin{align*}
u_1u_2aw & \in t \ U \\
= & \quad \{ (11) \text{ and } t \in t \ T \}
\end{align*}
\]

\[
\begin{align*}
w & \in t \ U \\
= & \quad \{ (11) \}
\end{align*}
\]

\[
\begin{align*}
u_1u_2aw & \in t \ U \\
= & \quad \{ (11) \text{ and } t \in t \ T \}
\end{align*}
\]

\[
\begin{align*}
(11) & \Rightarrow \{ \text{ R}_2, \text{ since } a \in o \ T \text{ and } u_2 \in o \ T^* \text{ according to (12) and (13) } \}
\end{align*}
\]

\[
\begin{align*}
u_1u_2aw & \in t \ U \\
= & \quad \{ (18) \}
\end{align*}
\]

\[
\begin{align*}
t_1a_2aw & \in t \ T \\
= & \quad \{ (11) \text{ and } t \in t \ T \}
\end{align*}
\]

\[
\begin{align*}
t_1a_2aw & \in t \ T \land t_1a_2aw \in t \ T \\
\Rightarrow & \quad \{ \text{ Lemma 2.7, since } t_2 \in o \ U^* \text{ according to (13), } a \in o \ T \text{ according to (12), and } \varepsilon \in o \ U \}
\end{align*}
\]

\[
\begin{align*}
t_1a_2aw & \in t \ T
\end{align*}
\]

and for any $\varepsilon \in o \ U$

\[
\begin{align*}
w & \in t \ U \\
= & \quad \{ (11) \}
\end{align*}
\]

\[
\begin{align*}
u_1u_2aw & \in t \ U \\
= & \quad \{ (11) \text{ and } t \in t \ T \}
\end{align*}
\]

\[
\begin{align*}
(11) & \Rightarrow \{ \text{ R}_2, \text{ since } a \in o \ T \text{ and } u_2 \in o \ T^* \text{ according to (12) and (13) } \}
\end{align*}
\]

\[
\begin{align*}
u_1u_2aw & \in t \ U \\
= & \quad \{ (18) \}
\end{align*}
\]

\[
\begin{align*}
t_1a_2aw & \in t \ T \\
= & \quad \{ (11) \text{ and } t \in t \ T \}
\end{align*}
\]

\[
\begin{align*}
t_1a_2aw & \in t \ T \land t_1a_2aw \in t \ T \\
\Rightarrow & \quad \{ \text{ Lemma 2.7, since } t_2 \in o \ U^* \text{ according to (13), } a \in o \ T \text{ according to (12), and } \varepsilon \in o \ U \}
\end{align*}
\]

\[
\begin{align*}
t_1a_2aw & \in t \ T
\end{align*}
\]
\[ \{ (11) \} \]
\[ u \in tT \]
(End of Proof)

Proof of Lemma 4.3: We prove for composable traces \( t \equiv tT \) and \( u \in tU \)
\[ (\forall u_0: u_0 \in \text{from}(t, u) : \text{mm}(t, u) \in tT) \wedge (\forall t_0 : t_0 \in \text{from}(u, t) : t_0 \in tT) \]
by mathematical induction on \( 1t + 1u + \text{mm}(t, u) \).

Base: \( 1t + 1u + \text{mm}(t, u) \leq 1 \). Obvious, since for composable traces \( 1t + 1u + \text{mm}(t, u) \neq 1 \), and since \( \epsilon \in tT \) and \( \epsilon \in tU \).

Step: We assume, given an integer \( k \), \( k > 1 \), that for composable traces \( t \in tT \) and \( u \in tU \) with \( 1t + 1u + \text{mm}(t, u) \leq k \)
\[ (\forall u_0 : u_0 \in \text{from}(t, u) : u_0 \in tU) \wedge (\forall t_0 : t_0 \in \text{from}(u, t) : t_0 \in tT) \] (6)

Let \( t \in tT \) and \( u \in tU \) be composable traces such that
\[ 1t + 1u + \text{mm}(t, u) = k + 1 \] (1)

As in the proof of Lemma 4.5, we may assume, due to the symmetric formulation of this lemma
\[ t = t_2t_1 \quad \text{and} \quad u = u_2u_1 \] (2)
\[ c(t_2t_1, u_2u_1) \] (3)
\[ a \in \circ T \] (4)
\[ t_1 \in \circ U \] \( \wedge \) \[ u_1 \in \circ U \] (5)
\[ c(t_0, u_0) \] (6)

From (1), (2), (3), and (5) we infer, using Lemma 4.0 and Property 4.1 (iii)
\[ 1(t_2t_1) + 1u_0 + \text{mm}(t_2t_1, u_0) = k + 1 \] (7)

From (4), (6), and (7) we infer
\[ 1(t_0) + 1u_0 + \text{mm}(t_0, u_0) = k - 1 \] (8)

We have to prove for traces \( t_2 \in \text{from}(u, t) \) and \( u_2 \in \text{from}(t, u) \) that \( t_2 \in tT \) and \( u_2 \in tU \). Let
\[ t_2 \in \text{from}(u, t) \] and \( u_2 \in \text{from}(t, u) \) (9)
4.2. Absence of Transmission and Computation Interference

Now we derive

true

= \{ (2) and (9) \}

\( t_2 \in \text{from}(u_0 t_1, t_0 t_2) \)

\imp \{ Property 4.1 (v), using \( u_1 \in o T^* \) according to (3) \}

\( t_2 \in \text{from}(u_0 t_1, t_0 t_2) \)

\imp \{ Property 4.1 (iv), using \( t_1 \in o U^* \) according to (5) \}

\( t_1 t_2 \in \text{from}(u_0, t_0 t_2) \)

\imp \{ definition of from, using \( a \in o T \) according to (4) and \( c(t_0, u_0) \) according to (6) \}

\( t_1 t_2 \in \text{from}(u_0, t_0) \)

\imp \{ induction hypothesis, using (6) and (8) \}

\( t_0 t_2 \in t \)

\( t_0 t_2 \in t \)

\imp \{ (2), using the prefix-closedness of \( t T \), and (4) and (11) \}

\( t_0 t_2 \in t \land t_0 t_2 \in t \land t_0 t_2 \in o U^* \)

\imp \{ Lemma 2.2 \}

\( t_0 t_2 \in t \land t_0 t_2 \in t \land t_0 t_2 \in t \)

\imp \{ (2) \}

\( t_0 t_2 \in t \land t_0 t_2 \in t \)

This means that we have proved half of the lemma, viz. \( u_2 \in t \).

In the same way as we derived \( t_1 t_2 \in \text{from}(u_0, t_0 t_2) \) (cf. (10)), we can also derive

\( u_1 u_2 \in \text{from}(t_0 t_2, u_0) \)  \( (14) \)

The traces of \( \text{from}(t_0, u_0) \) contain one symbol less than the traces of \( \text{from}(t_0 t_2, u_0) \), since \( a \in o T \) according to (4). Let

\( u_3 \in \text{from}(t_0, u_0) \)  \( (15) \)

Then we have that \( u_3 a \in \text{from}(t_0 t_2, u_0) \) and, hence, according to (14) and the definition of \( \text{from} \), that \( u_1 u_2 \) is a permutation of \( u_3 a \). We have to prove \( u_3 a \in t U \) or, equivalently by (2), \( u_3 u_1 u_2 \in t U \). By \( R_3 \) it now suffices to prove \( u_3 a \in t U \), since all symbols of \( u_3 a \) are of the same type. We derive

true

= \{ (15) \}

\( u_3 \in \text{from}(t_0, u_0) \)
\[ u_3 \in \text{from}(t_0, u_0) \land u_0 \mu_2 \in t \mathcal{U} \]
\[ \Rightarrow \{ \text{induction hypothesis, using (6) and (8) } \}
\[ \Rightarrow \{ (11) \text{ and (12) } \}
\[ \Rightarrow \{ \text{Property 4.1 (i), (ii), and (v), using the definition of from } \}
\[ \Rightarrow \{ \text{Lemma 4.5, using } 1(t_0) + 1(u_0) = k - 1, \text{ which we derive from (8) and Property 4.1 (iii) } \}
\[ \Rightarrow \{ \text{instantiation, using } \sigma \in \mathcal{O}^\mathcal{T}, \text{ according to (4), and } t_0 t_2 \in t \mathcal{T} \text{ according to (15) } \}
\[ u_0 \mu_2 \in t \mathcal{U} \]
\[ \text{(End of Proof) } \]

### 4.3. Blending as a composition operator

In the previous section we have proved the absence of transmission and computation interference. In this section we argue that blending as a composition operator is a proper abstraction of the mechanistic appreciation of composition as discussed earlier. We consider this a sufficient justification for using blending as a composition operator for composing \( C_4 \)'s by means of independent alphabets.

In the remainder of this section \( T \) and \( U \) are connectable \( C_4 \)'s. We define for two composable traces the set of resulting traces in the following way.

**Definition 4.3:** For traces \( t, u \in t \mathcal{U}, \) and \( x \) we say that \( x \) is a resultant of \( t \) and \( u, \) denoted by \( x \tau(t, u), \) if
\[ x \in \epsilon \land t = \epsilon \land u = \epsilon \land \]
\[ (\exists x_0, x_0, t_0 : x = x_0 a \land t = t_0 a \land x_0 \tau(t_0, u) \land (a \in \mathcal{O} \Rightarrow \not\exists_x a_1 > \#_x t)) \lor \]
\[ (\exists x_0, x_0, u_0 : x = x_0 a \land u = u_0 a \land x_0 \tau(t_0, u) \land (a \in \mathcal{O} \Rightarrow \#_x t > \#_x t_0)) \]

(End of Definition)
Composability of traces \( t \in T \) and \( u \in U \) equals \( (\exists x :: x \in \{ t, u \}) \). In the remainder of this section the set \( \{ x, t, u : t \in T \land u \in U \land x \in \{ t, u \} : x \} \) is denoted by \( S \). In view of our mechanistic appreciation it seems reasonable to define the specification of the composite to be \( S \models (aT + aU) \). We shall prove that this specification is equal to \( T \models bU \). To that end we observe the following.

Any trace in \( T \in U \) in which all symbols common to \( T \) and \( U \) are doubled belongs to \( S \) as can be proved by induction. Therefore, \( T \models bU \subseteq S \models (aT + aU) \). Proving that \( S \models (aT + aU) \subseteq T \models bU \) is more elaborate. At several places it involves induction. We choose for giving an outline of the proof rather than a fully detailed argument, since the latter would in no way contribute to our understanding of the theory developed in this monograph.

Occurrences of symbols in traces are counted from the left starting from 1.

Due to the absence of transmission interference, an odd occurrence of a symbol from \( aT \in aU \) in a trace of \( S \) originates from the trace structure where this symbol is an output symbol. In the same way we infer that an even occurrence of a common symbol stems from the trace structure where this symbol is an input symbol. Therefore, since the origin of non-common symbols is obvious, an \( x \in S \) can uniquely be unravelled into traces \( t \in T \) and \( u \in U \) such that \( x = (t, u) \). The unravelling can be effectuated by projecting on a composing trace structure’s alphabet and omitting the odd occurrences of a common symbol if it is an input symbol for this trace structure, and the even occurrences in case of an output symbol.

We prove that an arbitrary trace \( t \in S \) can be transformed, without affecting its projection on \( aT + aU \), into a trace in \( T \models bU \). As a consequence, \( S \models (aT + aU) \) is a subset of \( T \models bU \), which was the remaining proof obligation. The first step in this transformation is extending \( x \) with the common symbols of \( T \) and \( U \) that occur in \( x \) an odd number of times. The resulting trace belongs to \( S \) due to the absence of computation interference.

The next step is shifting to the left every even occurrence of a common symbol until it is adjacent to the preceding occurrence of that symbol. In the next paragraphs we show that the resulting trace still belongs to \( S \). Assuming this to hold, we first discuss the final step. Due to steps one and two, all common symbols occur in pairs. Therefore, the unravelling discussed above is the same as projecting on a composing trace structure’s alphabet after having replaced each such pair by a single symbol. Hence, this replacement yields a trace in \( T \models bU \). In none of the steps have we tampered with the non-common symbols and, hence, \( S \models (aS + aT) \) is unaffected.

There remains one assumption to be proved, viz., that the trace after shifting still belongs to \( S \). Let \( x \in S \), such that \( a \in aS \cap aT \), \( b \in aS \cup aT \), \( a \neq b \), and such that this occurrence of \( a \) is even. We prove that \( x \models abx \) is an element of \( S \) as well. By repeatedly applying this interchange for symbols to be shifted to the left it can be seen that our assumption indeed holds. We distinguish two cases: (i) these occurrences of \( a \) and \( b \) originate from two distinct
trace structures, and (ii) they originate from the same trace structure.

(i) Without loss of generality we assume the unravelling to result in traces $t_{o}t_{1} \in T$ and $u_{o}u_{1} \in U$ such that $x_{o}(t_{1}, u_{1})$. Since $x_{o}u_{1} \in S$, we infer from Definition 4.3 that $b \in oT \Rightarrow \pi_{b}b > \pi_{a}a_{0}$ and that $a \in oU \Rightarrow \pi_{a}a_{0} > \pi_{b}b_{0}$. Since $a \neq b$, we derive $a \in oU \Rightarrow \pi_{a}a_{0} > \pi_{b}b_{0}$ and $b \in oT \Rightarrow \pi_{b}b_{1} > \pi_{a}a_{0}$, which implies $x_{o}b_{1} \in (t_{o}a_{1}, u_{o}b_{1})$. Moreover, it can be seen from this definition that the construction of $x_{1}$ depends on $t_{1}$, $u_{1}$, and the number of times each symbol occurs in $x_{o}b_{o}$ only and, hence, not on the ordering of symbols in $x_{o}b_{o}$. This implies that also $x_{o}b_{o}x_{1} \in S$.

(ii) Without loss of generality we assume the unravelling of $x_{o}b_{o}x_{1}$ to result in traces $t_{o}b_{o}t_{1} \in T$ and $u_{o}b_{o}u_{1} \in U$ such that $x_{o}(t_{o}, u_{1})$. It suffices to prove that $t_{o}b_{o}t_{1} \in T$; since this implies $x_{o}b_{o} \in S$ and since the construction of $x_{1}$ does not depend on the ordering of symbols in $x_{o}b_{o}$. This occurrence of $a$ is even in $x_{o}b_{o}$, hence, $a \in oT \cap oU$. If $b \in oT$ then $t_{o}b_{o}t_{1} \in T$ on account of $R_{3}$. Therefore, assume $b \in oT$. From Definition 4.3 it can be seen that $\pi_{b}b_{0} \geq \pi_{a}a_{0}$, which implies $\pi_{a}a_{0} \geq \pi_{b}b_{0}$. Due to the absence of computation interference we conclude $t_{o}b_{o} \in T$, and, applying $R_{3''}$, $t_{o}b_{o} \in T$. We prove $t_{o}b_{o}t_{1} \in T$ for an arbitrary prefix $t_{2}$ of $t_{1}$. For $t_{2} = \epsilon$ it is obvious. If $t_{2}$ is a prefix of $t_{1}$ such that $t_{o}b_{o}t_{2} \in T$ then we distinguish the following three cases (using $t_{o}b_{o}t_{2} \in T$ on account of the prefix-closedness of $T$). If $c \in oT$ then $t_{o}b_{o}t_{2} \in T$ on account of $R_{4}$, if $c \in T \setminus (aT \cap aU)$ then $t_{o}b_{o}t_{2} \in T$, since $aT \setminus (aT \cap aU)$ is independent with respect to $T$ and $a \neq aT \setminus (aT \cap aU)$. If $c \in T \cap oU$ then $t_{o}b_{o}t_{2} \in T$ on account of the absence of computation interference.
5

Closure properties

In this chapter we discuss the closure of the four classes under composition of connectable trace structures. It turns out that all but $C_2$ are closed under composition. In a number of examples we apply the theory thus far developed and derive specifications of the composite from the specifications of the composing parts.

We begin this chapter with a section that contains a number of lemmata for trace structures obtained by weaving. Most of these lemmata are counterparts of lemmata in Chapters 2 and 3 on the shifting of symbols. In Section 5.1 we show that a composite obtained by weaving satisfies the rules for delay-insensitivity, provided that the composing parts do. The next section deals with $R_3$ through $R_7$ for a composite obtained by blending. In order to prove the $R_i$'s and $R_3$'s, which is done in Sections 5.4, 5.5, and 5.6, we need a better understanding of the relation between the weave and the blend of two trace structures. This is explored in Section 5.3. By this exploration the crucial distinction between $C_2$ and $C_3$ becomes clear.

In the proofs of this chapter we frequently use the definition of weaving. Part of this definition concerns the domain of the traces considered. For the sake of brevity we omit these domain concerns, appealing to the willingness of the reader to add them at the appropriate places.

5.0. Shifting symbols in trace structures obtained by weaving

The lemmata in this section are counterparts of lemmata in Chapters 2 and 3 on the shifting of symbols. Most of the proofs are merely applications of the corresponding lemmata in these chapters. Therefore, we prove a few lemmata in detail, assuming that this provides a sufficient clue for the derivation of the remaining proofs.
Lemma 5.0: (cf. Lemma 3.1) For connectable C's S and T, for traces s, t, and u, and for symbol a \in \Sigma(S \uplus T) such that t \leftarrow (a S \uplus a T) = ε

\text{stau} \in t(S \uplus T) \Rightarrow \text{stau} \in t(S \uplus T)

Proof: By Property 3.0 we assume without loss of generality

a \in S \setminus a T

Now we derive

\text{stau} \in t(S \uplus T)

= \{ \text{definition of weaving} \}

\text{stau} [a S \in t S \land \text{stau} [a T \in t T]

= \{ \text{distribution of projection over concatenation, using (0)} \}

(t [a S] (t [a S] a (u [a S]) \in t S \land \text{stau} [a T \in t T]

\Rightarrow \{ \text{since } t \leftarrow (a S \uplus a T) = ε, \text{we have } t \leftarrow (a S) [(a S \setminus a T) = ε. Moreover,}

a S \setminus a T \text{ is independent with respect to } S, \text{ due to the connectability of } S

and \ T. \text{ Hence, we may apply Lemma 3.1} \}

(t [a S] a (t [a S] a (u [a S]) \in t S \land \text{stau} [a T \in t T]

= \{ \text{distribution of projection over concatenation, using (0)} \}

\text{stau} [a S \in t S \land \text{stau} [a T \in t T]

= \{ \text{definition of weaving} \}

\text{stau} \in t(S \uplus T)

(End of Proof)

In exactly the same way we derive the next lemma.

Lemma 5.1: (cf. Lemma 3.2) For connectable C's S and T, for traces s, t, and u, and for symbol a \in \Sigma(S \uplus T) such that t \leftarrow (a S \uplus a T) = ε

\text{stau} \in t(S \uplus T) \Rightarrow \text{stau} \in t(S \uplus T)

From Lemmata 5.0 and 5.1 we derive
Lemma 5.2: (cf. Lemma 3.3) For connectable $\mathcal{C}_4$'s $S$ and $T$, for traces $s$, $t$, and $u$, and for symbols $a \in a S \supset a T$ and $b \in a S \supset a T$ of the same type such that $t \models (a S \supset a T) = \epsilon$

$sabu \in t(S w T) \Rightarrow sabu \in t(S w T) \lor stabu \in t(S w T)$

Lemma 5.3: (cf. Lemma 2.8) For connectable $\mathcal{C}_4$'s $S$ and $T$, for traces $s$ and $t$, and for symbol $a \in a S \cap a T$ such that $i \models \{a\} = \epsilon$

$s \in t(S w T) \land st \in t(S w T) \Rightarrow sta \in t(S w T)$

Proof: Without loss of generality we assume

$a \in o S \cap i T$ (0)

We prove that the left-hand side implies (i) $sta \models a S \in t S$ and (ii) $sta \models a T \in t T$, which implies, by the definition of weaving, the right-hand side.

(i) $sta \models a S \in t S$

$s \in t(S w T) \land st \in t(S w T)$

$\Rightarrow \{ \text{ calculus and definition of weaving } \}$

$s \models a S \in t S \land st \models a S \in t S$

$= \{ \text{ distribution of projection over concatenation, using (0) } \}$

$(s \models a S) a \in t S \land (s \models a S) t \models a S \in t S$

$\Rightarrow \{ \text{ from } i \models \{ a \} = \epsilon \text{ we infer } t \models a S \models \{ a \} = \epsilon. \text{ Hence, since } a \in o S, \text{ we may apply Lemma 2.8 } \}$

$(s \models a S) t \models a S \in t S$

$= \{ \text{ distribution of projection over concatenation, using (0) } \}$

$sta \models a S \in t S$

(ii) $sta \models a T \in t T$

$s \in t(S w T) \land st \in t(S w T)$

$\Rightarrow \{ \text{ calculus and definition of weaving, and (i) } \}$

$st \models a T \in t T \land sta \models a S \in t S$

$\Rightarrow \{ \text{ distribution of projection over concatenation, using (0), and projection on } a S \cap a T \}$

$sta \models a T \in t T \land (st \models a S) t \models (a S \cap a T) \in t S \models (a S \cap a T)$
\[= \{\text{Property 1.1, using } aS \cap aT \subseteq aS \text{ and } aS \cap aT \subseteq aT\}\]
\[\text{st} \{aT \in tT \land (sT \in aT)\} \Leftrightarrow (aS \cap aT) \subseteq tS \subseteq (aS \cap aT)\]
\[= \{tS[(aS \cap aT) = tT[(aS \cap aT)], \text{since } S \text{ and } T \text{ are connectable}\}\]
\[\text{st} \{aT \in tT \land (sT \in aT)\} \Leftrightarrow (aS \cap aT) \subseteq tT \subseteq (aS \cap aT)\]
\[= \{aS \cap aT \text{ is independent with respect to } T \text{ and } a \in (aS \cap aT) \cap iT\}
\text{according to (0)}\}
\[\text{st} \{aT\}a \in tT\]
\[= \{\text{distribution of projection over concatenation, using (0)}\}\]
\[\text{st} \{aT \in tT\}\]

(End of Proof)

Notice that we used here explicitly, as we will in the next proof as well, the last requirement for connectability, viz. \(S[(aS \cap aT) = T[(aS \cap aT)]\).

**Lemma 5.4**: (cf. Lemma 2.9) For connectable C_2's \(S\) and \(T\), for traces \(s, t,\) and \(u,\) and for symbol \(a \in aS \cap aT\) such that \(t[\{a\}] = \epsilon\)
\[sa \in t(SwT) \land stu \in t(SwT) \Rightarrow satu \in t(SwT)\]

**Proof**: Without loss of generality we assume
\[a \in aS \cap iT\]

Again we prove \(stau[aS \in tsS]\) and \(stau[aT \in tT]\) separately.

(i) \(stau[aS \in tsS]\)
\[sa \in t(SwT) \land stu \in t(SwT)\]
\[\Rightarrow \{\text{definition of weaving and calculus}\}\]
\[sa[aS \in tsS \land stu[aS \in tsS]\]
\[= \{\text{distribution of projection over concatenation, using (0)}\}\]
\[t[aS]a \in tsS \land (sT \in aS)(t[aS]a(u[aS]) \in tsS\]
\[\Rightarrow \{\text{from } t[\{a\}] = \epsilon \text{ we infer } t[aS][\{a\}] = \epsilon. \text{ Hence, since } a \in aS, \text{ we may apply Lemma 2.10}\}\]
\[(\forall w, w_1 : w \preceq w_1 \Rightarrow t[aS:w_1w_2a(u[aS]) \in tsS]\]
\[\Rightarrow \{\text{instantiation}\}\]
\[t[aS]a(t[aS])u[aS] \in tsS\]
(ii) \(satu\{aT \in tT\)
\(sa \in t(S \cap aT) \land stau \in t(S \wedge aT)\)

\[\rightarrow (\text{definition of weaving, (1), and calculus})\]

\(stau \in tS \in tT \land (\forall w_0, w_1 : w_0 w_1 = \emptyset [aS : (s [aS]a_0 w_0 (s [aS]a_0 T) \in tS \cap (aS \cap aT)])\)

\[\rightarrow (\text{of prefix-closed and projection on } aS \cap aT)\]

\(stau \in tS \in tT \land (\forall w_0, w_1 : w_0 w_1 = \emptyset [aS : (s [aS]w_0 a_0 (aS \cap aT) \in tS \cap (aS \cap aT)])\)

\[= (\text{the set } \{w_0, w_1 : w_0 w_1 = \emptyset [aS : (s w_0 (aS \cap aT)) \in tT \cap (aS \cap aT)])\}

\(= (\text{distribution of projection over concatenation, using (0) and Property 1.1; } tT \text{ is prefix-closed})\)

\(t[aT](t[aT]u [aT] \in tT \land
(\forall w_0, w_1 : w_0 w_1 = \emptyset [aT : (s [aT]w_0 a_0 \in tT \land
(s [aT]w_0 a_0 (aS \cap aT) \in tT \cap (aS \cap aT)])\)

\[= (\text{distribution of projection over concatenation, using (0) and Property 1.1})\]

\(t[aT](t[aT]u [aT] \in tT \land
(\forall w_0, w_1 : w_0 w_1 = \emptyset [aT : (s [aT]w_0 a_0 \in tT \land
(s [aT]w_0 a_0 (aS \cap aT) \in tT \cap (aS \cap aT)])\)

\[\Rightarrow (\text{Lemma 2.11, since } a \in tT \text{ according to (0))}\]

\(\forall w_0, w_1 : w_0 w_1 = \emptyset [aT : (s [aT]w_0 a_0 \in tT \land
(s [aT]w_0 a_0 (aS \cap aT) \in tT \cap (aS \cap aT)])\)

\[\Rightarrow (\text{instantiation and distribution of projection over concatenation, using (0) })\]

\(satu \{aT \in tT\}\)

\[(\text{End of Proof})\]
On account of Lemmata 5.3 and 5.4 we may, given two connectable $C_2$'s $S$ and $T$, symbol $a \in a S \cap a T$ and traces $st$ and $st'$ in $t(S \omega T)$, shift the leftmost $a$ in $t$ to the left of $t'$, or, if no such $a$ exists, insert an $a$ between $s$ and $t$. Therefore, the following corollary is a straightforward application of these two lemmata.

**Corollary 5.0**: For connectable $C_2$'s $S$ and $T$, for traces $s$, $t$, and $u$, and for symbols $a \in a S \cap a T$ and $b$ such that $b \neq a$

$$sa \in t(S \omega T) \land s t u b \in t(S \omega T) \Rightarrow$$

$$(\exists w_0, w_1 : s a w_0 a e \in t(S \omega T) \land w_0, w_1 \{ (a S + a T) = t \} (a S + a T) \land w_1 \{ (a S + a T) = u \} (a S + a T) \land 1 w_0 \leq 1 t)$$

We conclude this section with two lemmata which are quite similar to Lemmata 5.3 and 5.4 but much easier to prove. The distinction is that symbol $a$ is an element of $a S \omega T$ rather than of $a S \cap a T$.

**Lemma 5.5**: (cf. Lemma 2.8) For connectable $C_2$'s $S$ and $T$, for traces $s$ and $t$, and for symbol $a \in a S \omega T$ such that $t \{ a \} = \epsilon$

$$sa \in t(S \omega T) \land st \in t(S \omega T) \Rightarrow stu \in t(S \omega T)$$

**Lemma 5.6**: (cf. Lemma 2.9) For connectable $C_2$'s $S$ and $T$, for traces $s$, $t$, and $u$, and for symbol $a \in a S \omega T$ such that $t \{ a \} = \epsilon$

$$sa \in t(S \omega T) \land stau \in t(S \omega T) \Rightarrow s t u \in t(S \omega T)$$

5.1. $R_2$ through $R_5$ for trace structures obtained by weaving

In this section we show that $R_2$ through $R_5$ hold for the composite obtained by weaving of connectable trace structures. Most of the proofs merely require a frequent use of distribution of projection over concatenation and of the definitions of weaving and the four classes. Therefore, we prove only some of the lemmata.
5.1. \( R_2 \) THROUGH \( R_5 \) FOR TRACE STRUCTURES OBTAINED BY WEAVING

**Lemma 5.7** (cf. \( R_2 \)) For connectable \( C_4 \)'s \( S \) and \( T \), for trace \( s \), and for symbol \( a \)

\[
sas \in t(S \cdot w \cdot T)
\]

**Lemma 5.8** (cf. \( R_3 \)) For connectable \( C_4 \)'s \( S \) and \( T \), for traces \( s \) and \( t \), and for symbols \( a \in aS \cdot \cdot \cdot aT \) and \( b \in aS \cdot \cdot \cdot aT \) of the same type

\[
sabt \in t(S \cdot w \cdot T) = sbat \in t(S \cdot w \cdot T)
\]

**Proof**: Without loss of generality we assume \( a \in aS \). We distinguish two cases
(i) \( b \in aS \), and (ii) \( b \not\in aS \).

(i) Since \( a \in aS \cdot \cdot \cdot aT \) and \( b \in aS \cdot \cdot \cdot aT \) we have in this case

\[
s \in aS \setminus aT \quad \text{and} \quad b \not\in aS \setminus aT
\]

Now we derive

\[
sabt \in t(S \cdot w \cdot T)
\]

\[
= \quad \text{(definition of weaving)}
\]

\[
sabt \left[ aS \in tS \right] \wedge sbat \left[ aT \in tT \right]
\]

\[
= \quad \text{(distribution of projection over concatenation, using (0))}
\]

\[
(\cdot [aS]ab(t[aS]) \in tS \wedge st[aT] \in tT)
\]

\[
= \quad \text{\( R_4 \), since \( S \) is a \( C_4 \) and \( a \) and \( b \) are of the same type)}
\]

\[
(\cdot [aS]ab(t[aS]) \in tS \wedge st[aT] \in tT)
\]

\[
= \quad \text{(distribution of projection over concatenation, using (0))}
\]

\[
sbat \left[ aS \in tS \right] \wedge sbat \left[ aT \in tT \right]
\]

\[
= \quad \text{(definition of weaving)}
\]

\[
sbat \in t(S \cdot w \cdot T)
\]

(ii) In this case we have \( a \in aS \setminus aT \) and \( b \not\in aT \setminus aS \). Now Property 1.3 yields the result desired.

(End of Proof)
Lemma 5.9: (cf. $R_4$) For connectable $C_4$’s $S$ and $T$, for traces $s$ and $t$, and for symbols $a \in aS + aT$ and $b \in aS + aT$ of different types

$$na \in t(S \ast w \ast T) \setminus \text{shat} \in t(S \ast w \ast T) \Rightarrow \text{shat} \in t(S \ast w \ast T)$$

Lemma 5.10: (cf. $R_4''$) For connectable $C_4$’s $S$ and $T$, for traces $s$ and $t$, and for symbols $a \in aS + aT$, $b \in aS + aT$, and $c \in aS + aT$ such that $b$ is of another type than $a$ and $c$

$$sabc \in t(S \ast w \ast T) \setminus \text{shat} \in t(S \ast w \ast T) \Rightarrow \text{shat} \in t(S \ast w \ast T)$$

Proof: We distinguish three cases: (i) $a$, $b$, and $c$ belong to the same trace structure, (ii) $c$ belongs to another trace structure than $a$ and $b$, and (iii) $a$ and $b$ belong to different trace structures.

(i) $a$, $b$, and $c$ belong to the same trace structure. Without loss of generality we assume this trace structure to be $S$. Hence,

$$u \in aS \setminus aT, \quad b \in aS \setminus aT, \quad and \quad c \in aS \setminus aT$$

(0)

Now we derive

$$sabc \in t(S \ast w \ast T) \setminus \text{shat} \in t(S \ast w \ast T)$$

$$= \{ \text{definition of weaving} \}$$

$$sabc \in t(S \ast w \ast T) \setminus \text{shat} \in t(S \ast w \ast T)$$

$$= \{ \text{distribution of projection over concatenation, using (0); calculus} \}$$

$$\Rightarrow \{ \text{R}_4'' \}$$

(0)

(ii) $c$ belongs to another trace structure than $a$ and $b$. Without loss of generality we assume $c \in aT$. Hence,

$$a \in aS \setminus aT, \quad b \in aS \setminus aT, \quad and \quad c \in aT \setminus aS$$

(1)

Now we derive
5.1. \( r_n \) through \( r_3 \) for trace structures obtained by weaving

\[
\text{subtc} \in t(S \cdot w \cdot T) \land \text{shat} \in t(S \cdot w \cdot T)
\]

\[
= \{ \text{definition of weaving} \}
\]

\[
\text{shat} \mid a \in S \land \text{shat} \mid a \in T
\]

\[
= \{ \text{calculus and distribution of projection over concatenation, using (1)} \}
\]

\[
\text{shat} \mid a \in S \land \text{shat} \mid a \in T
\]

\[
= \{ \text{definition of weaving} \}
\]

\[
\text{shat} \in t(S \cdot w \cdot T)
\]

(iii) \( a \) and \( b \) belong to different trace structures. Then, according to Property 1.3, \( \text{subtc} \in t(S \cdot w \cdot T) \Rightarrow \text{shat} \in t(S \cdot w \cdot T) \).

(End of Proof)

Lemma 5.11: (cf. \( R_n \)) For connectable \( C_4 \)'s \( S \) and \( T \), for traces \( s \) and \( t \), and for symbols \( a \in o(S \cdot w \cdot T), b \in i(S \cdot w \cdot T), \) and \( c \in e \cdot a \cdot S \land a \cdot T \)

\[
\text{subtc} \in t(S \cdot w \cdot T) \land \text{shat} \in t(S \cdot w \cdot T) \Rightarrow \text{shat} \in t(S \cdot w \cdot T)
\]

Proof: We distinguish two cases: (i) \( a \) and \( b \) belong to the same trace structure, and (ii) \( a \) and \( b \) belong to different trace structures.

(i) \( a \) and \( b \) belong to the same trace structure. Without loss of generality we assume this trace structure to be \( S \). Hence,

\[
a \in o \cdot S \land a \cdot T \quad \text{and} \quad b \in i \cdot S \land a \cdot T
\]

(0)

Next, we distinguish (a) \( c \in i \cdot S \land o \cdot T \), and (b) \( c \in o \cdot S \land i \cdot T \)

(a) \( c \in i \cdot S \land o \cdot T \). Now we derive

\[
\text{shat} \in t(S \cdot w \cdot T) \land \text{shat} \in t(S \cdot w \cdot T)
\]

\[
= \{ \text{definition of weaving} \}
\]

\[
\text{shat} \mid a \in S \land \text{shat} \mid a \in T
\]

\[
= \{ \text{distribution of projection over concatenation, using (0). Moreover, projection on } a \cdot S \land a \cdot T, \text{ using Property 1.1, } c \in e \cdot a \cdot S \land a \cdot T, \text{ and (0) } \}
\]

\[
(s \mid [a \cdot S] \cdot [a \cdot T]) \in t(S \cdot [a \cdot S] \land [a \cdot T]) \land (s \mid [a \cdot S] \cdot [a \cdot T]) \in t(S \cdot [a \cdot S] \land [a \cdot T])
\]
\[
\begin{align*}
&= \{ aS \cap aT \text{ is independent with respect to } S \text{ and } e \in (aS \cap aT) \cap iS \} \\
&\quad (s [aS])b\langle t [aS] \rangle e \in ts \land stc [aT \subseteq tT] \\
&= \{ \text{distribution of projection over concatenation, using (0) and } e \in aS \} \\
&\quad sbat [aS \subseteq ts] \land sbat [aT \subseteq tT] \\
&= \{ \text{definition of weaving} \} \\
&\quad sbat \in t(SwT) \\

(b) \quad e \in oS \cap iT \\
&\quad sbat \in t(SwT) \land sbat \in t(SwT) \\
&= \{ \text{definition of weaving} \} \\
&\quad sbat [aS \subseteq ts] \land sbat [aT \subseteq tT] \land sbat [aS \subseteq tS] \land sbat [aT \subseteq tT] \\
&\Rightarrow \{ \text{distribution of projection over concatenation, using (0) and } e \in aS \} \\
&\quad (s [aS])b\langle t [aS] \rangle e \in ts \land stc [aT \subseteq tT] \land (s [aS])b\langle t [aS] \rangle e \in ts \\
&\Rightarrow \{ R_{sy}'' \text{, since } a \in oS \text{ and } b \in iS \text{ according to (0), and } e \in oS \} \\
&\quad (s [aS])b\langle t [aS] \rangle e \in ts \land stc [aT \subseteq tT] \\
&= \{ \text{distribution of projection over concatenation, using (0) and } e \in aS \} \\
&\quad sbat [aS \subseteq ts] \land sbat [aT \subseteq tT] \\
&= \{ \text{definition of weaving} \} \\
&\quad sbat \in t(SwT) \\

(ii) \quad a \text{ and } b \text{ belong to different trace structures. Then, according to Property 1.3, } sbat \in t(SwT) = sbat \in t(SwT). \\
\text{(End of Proof)}
\end{align*}
\]

Lemma 5.12 : (cf. \( R_{s_y} \)) For connectable \( C_1 \)'s \( S \) and \( T \), for trace \( s \), and for distinct symbols \( a \in aS \cap aT \) and \( b \in aS \cap aT \)

\[
sa \in t(SwT) \land sb \in t(SwT) \Rightarrow sab \in t(SwT)
\]

Lemma 5.13 : (cf. \( R_{s_y}'' \)) For connectable \( C_2 \)'s \( S \) and \( T \), for trace \( s \), and for distinct symbols \( a \in aS \cap aT \) and \( b \in aS \cap aT \), not both belonging to \( i(SwT) \)

\[
sa \in t(SwT) \land sb \in t(SwT) \Rightarrow sab \in t(SwT)
\]
Lemma 5.14: (cf. R₃'') For connectable C₄'s S and T, for trace s, and for symbols a ∈ aS + a T and b ∈ aS + a T of different types

\[ sa \in t(S \bowtie T) \wedge sb \in t(S \bowtie T) \Rightarrow sab \in t(S \bowtie T) \]

5.2. R₀ through R₃ for trace structures obtained by blending

Using the lemmata derived in the preceding section it is easy to prove R₀ through R₃ for the composite of two connectable C₄'s. The proofs are short and straightforward and, therefore, all are omitted but one.

Lemma 5.15: For connectable C₄'s S and T

1) \( t(SbT) \cup a(SbT) = a(SbT) \)
2) \( t(SbT) \) is prefix-closed and non-empty
3) for trace s and symbol a ∈ a(SbT) \( \Rightarrow saa \in t(SbT) \)
4) for traces s and t, and for symbols a ∈ a(SbT) and b ∈ a(SbT) of the same type \( sab \in t(SbT) = sba \in t(SbT) \)

Proof of 3):

\[ \begin{align*}
    sab & \in t(SbT) \\
    & = \{ \text{definition of blending, using } a \in a(SbT) \text{ and } b \in a(SbT) \} \\
    & (\exists t_0, t_1, t_2 : t_0, t_1, t_2 \in t(SwT) \wedge t_0[(aS + a T) = s \wedge t_1[(aS + a T) = \epsilon \\
    & \wedge t_2[(aS + a T) = t \} \\
    & \Rightarrow \{ \text{Lemma 5.2, since } a \text{ and } b \text{ are of the same type} \} \\
    & (\exists t_0, t_1, t_2 : (t_0, t_1, t_2 \in t(SwT) \vee t_0, t_1, t_2 \in t(SwT)) \wedge \\
    & t_0[(aS + a T) = s \wedge t_1[(aS + a T) = \epsilon \wedge t_2[(aS + a T) = t \} \\
    & \Rightarrow \{ \text{Lemma 5.8} \} \\
    & (\exists t_0, t_1, t_2 : (t_0, t_1, t_2 \in t(SwT) \vee t_0, t_1, t_2 \in t(SwT)) \wedge \\
    & t_0[(aS + a T) = s \wedge t_1[(aS + a T) = \epsilon \wedge t_2[(aS + a T) = t \} \\
    & \Rightarrow \{ \text{definition of blending, using } a \in a(SbT) \text{ and } b \in a(SbT) \} \\
    & sab \in t(SbT) = sba \in t(SbT) \\
\end{align*} \]

Hence, \( sab \in t(SbT) \Rightarrow sba \in t(SbT) \) for symbols a and b of the same type. Therefore, the implication may be replaced by equality.

(End of Proof)
5.3. Internal communications for a blend

The remaining rules to be proved for the blend of two connectable trace structures are less easily derived from those for the weave. The reason is that in the left-hand sides of the implications in these rules the same trace occurs twice. By the standard conversion from an expression in terms of the blend to an expression in terms of the weave, these occurrences convert to possibly distinct traces. As a consequence, the lemmata derived in Section 5.1 are not readily applicable. Therefore, we prove in this section three lemmata that relate traces in the blend to traces in the weave in such a way that we can apply the lemmata derived in Section 5.1. Due to the absence of arbitration in the internal communications, we can prove for $C_2$'s a stronger lemma than for $C_4$'s.

Lemma 5.16 : For connectable $C_2$'s $S$ and $T$, for traces $s$, $t$, and $u$, and for symbols $a \in a S + a T$ and $b \in a S + a T$

\[ s a t \in t(S b T) \land s b u \in t(S b T) \implies \]

\[(3s_0, s_1, s_2 : s_3 a r_1 \in t(S w T) \land s_4 b r_2 \in t(S w T) \land s_5 [(a S + a T) = s \land s_1 [(a S + a T) = t \land s_2 [(a S + a T) = u \]

Proof : We prove by mathematical induction on $1 r_1 + 1 r_3$ that for traces $r_0, r_1, r_2, r_3,$ and $r_4$, such that

\[ r_1 [(a S + a T) = r_3 [(a S + a T) \]

we have

\[ r_0, r_2, r_4 \in t(S w T) \land r_0, r_2, r_4 \in t(S w T) \]

\[(3s_0, s_1, s_2 : s_3 a r_1 \in t(S w T) \land s_4 b r_2 \in t(S w T) \land s_5 [(a S + a T) = r_1 [(a S + a T) \land s_1 [(a S + a T) = r_2 [(a S + a T) \land s_2 [(a S + a T) = r_4 [(a S + a T)] \]

By choosing $r_0 = \epsilon$ we then have proved the theorem, since

\[ s a t \in t(S b T) \land s b u \in t(S b T) \implies \]

\[(3s_1, s_2, s_3, s_4 : s_5 a r_1 \in t(S w T) \land s_4 b r_2 \in t(S w T) \land s_5 [(a S + a T) = s \land s_1 [(a S + a T) = t \land s_2 [(a S + a T) = u \land s_4 [(a S + a T) = u \]

Base : $1 r_1 + 1 r_3 = 0$. (1) holds obviously in this case.

Step : Given integer $k, k > 0$. We assume (1) to hold for traces $r_0, r_1, r_2, r_3,$ and $r_4$ such that $1 r_1 + 1 r_3 < k$ and (0). For traces $r_0, r_1, r_2, r_3,$ and $r_4$ such that (0)
and such that
\[ l_1 + l_3 = k \]  
we prove (1) in the following way.

We distinguish two cases: (i) \( r_1 \) and \( r_2 \) start with the same symbol, and (ii) \( r_1 \) and \( r_2 \) do not start with the same symbol.

(i) \( r_1 \) and \( r_2 \) start with the same symbol, say \( r_1 = aS \) and \( r_2 = aT \). Then we may apply (1) with its \( r_0, r_1, \) and \( r_3 \) replaced by \( r_0, r_1, \) and \( r_3 \) respectively, since \( l_1 + l_3 < l_1 + l_3 \) (\( = k \)), and since we infer from (0) and the distribution of projection over concatenation \( r_3[(aS+aT) = r_5[(aS+aT)] \) (1) follows by a simple renaming.

(ii) \( r_1 \) and \( r_3 \) do not start with the same symbol. Moreover, they are not both equal to \( e \) according to (2) and the fact that \( k > 0 \). Hence, at least one of them starts with a symbol of \( aS \cap aT \), since \( r_1[(aS+aT)] = r_5[(aS+aT)] \) according to (0). Without loss of generality we assume \( r_1 \) to start with a symbol of \( aS \cap aT \), say
\[ r_1 = aS \]  and \( e \in aS \cap aT \)

Now we derive
\[ r_0[aS] \in t(SwT) \land r_0[aT] \in t(SwT) \]
\[ r_0[aS] \in t(SwT) \land r_0[aT] \in t(SwT) \]
\[ \Rightarrow \{ \text{Corollary 5.0, using } e \in aS \cap aT \} \]
\[ \Rightarrow \{ \text{Corollary 5.0, using } e \in aS \cap aT \} \]
\[ \Rightarrow \{ \exists u_0, u_1 : r_0[aS] \in t(SwT) \land r_0[aT] \in t(SwT) \land u_0 \in 1_0 \land u_0[(aS+aT) = r_5[(aS+aT) \land u_1[(aS+aT) = r_5[(aS+aT)] \} \]
\[ \Rightarrow \{ \exists u_0, u_1 : r_0[aS] \in t(SwT) \land r_0[aT] \in t(SwT) \land u_0 \in 1_0 \land u_0[(aS+aT) = r_5[(aS+aT) \land u_1[(aS+aT) = r_5[(aS+aT)] \} \]
\[ \Rightarrow \{ \text{Corollary 5.0, using } e \in aS \cap aT \} \]
\[ \Rightarrow \{ \exists u_0, u_1 : r_0[aS] \in t(SwT) \land r_0[aT] \in t(SwT) \land u_0 \in 1_0 \land u_0[(aS+aT) = r_5[(aS+aT) \land u_1[(aS+aT) = r_5[(aS+aT)] \} \]
\[ \Rightarrow \{ \text{Corollary 5.0, using } e \in aS \cap aT \} \]
\[ s_2[(a \cdot S \cdot a T) = w_1 [(a \cdot S \cdot a T) \wedge w_1 [(a \cdot S \cdot a T) = r_1[(a \cdot S \cdot a T)]
\]
\[ \Rightarrow \{ \text{calculus and renaming } c_{0_0}, \text{ using } (3) \}\]
\[ \exists \theta, s_1, s_2 : \theta_{0_0} \cdot \alpha_1 \in t(S \cdot w \cdot T) \wedge \theta_{0_0} \cdot \beta_2 \in t(S \cdot w \cdot T) \wedge
\]
\[ s_0[(a \cdot S \cdot a T) = r_1[(a \cdot S \cdot a T) \wedge s_1[(a \cdot S \cdot a T) = r_2[(a \cdot S \cdot a T)
\]
\[ \wedge s_2[(a \cdot S \cdot a T) = r_3[(a \cdot S \cdot a T)]
\]
\[ \text{(End of Proof)}
\]

**Lemma 5.17**: For connectable \( \mathcal{C}_a \)'s \( S \) and \( T \), for traces \( s, s_0, t, \) and \( t_0 \), and for symbols \( a \in \iota(S \cdot b \cdot T) \) and \( b \in \omega(S \cdot b \cdot T) \) such that \( s_0[(a \cdot S \cdot a T) = s \) and \( t_0[(a \cdot S \cdot a T) = t \)

\[ s_0 \cdot h \cdot t_0 \in t(S \cdot w \cdot T) \wedge s \cdot h \cdot t \in t(S \cdot b \cdot T) \Rightarrow s_0 \cdot h \cdot t_0 \in t(S \cdot w \cdot T)
\]

**Proof**: By mathematical induction on the length of \( t_0 \).

**Base**: \( t_0 = e \). Now we derive

\[ s_0 \cdot h \cdot t_0 \in t(S \cdot w \cdot T) \wedge s \cdot h \cdot t \in t(S \cdot b \cdot T)
\]
\[ \Rightarrow \{ t(S \cdot w \cdot T) \text{ and } t(S \cdot b \cdot T) \text{ are prefix-closed } \}
\]
\[ s_0 \cdot h \in t(S \cdot w \cdot T) \wedge s_0 \in t(S \cdot w \cdot T) \wedge h \in t(S \cdot b \cdot T)
\]
\[ \Rightarrow \{ \text{distribution of projection over concatenation, using } s = s_0[(a \cdot S \cdot a T)
\]
\[ \wedge h \in a(S \cdot b \cdot T) \}
\]
\[ s_0 \cdot h \in t(S \cdot w \cdot T) \wedge s_0 \in t(S \cdot w \cdot T) \wedge s_0 \cdot [h[[a \cdot S \cdot a T] = t(S \cdot b \cdot T)
\]
\[ \Rightarrow \{ \text{Lemma 3.5, using } a \in \iota(S \cdot b \cdot T) \text{ and the definition of blending } \}
\]
\[ s_0 \cdot h \in t(S \cdot w \cdot T) \wedge s_0 \cdot h \in t(S \cdot w \cdot T)
\]
\[ \Rightarrow \{ \text{Lemma 5.14, using } a \in t(S \cdot b \cdot T) \text{ and } b \in \omega(S \cdot b \cdot T) \}
\]
\[ s_0 \cdot h \in t(S \cdot w \cdot T)
\]
\[ = \{ t_0 = e \}
\]
\[ s_0 \cdot h \cdot t_0 \in t(S \cdot w \cdot T)
\]

**Step**: \( t_0 = t_1 \cdot t \). We distinguish two cases: (i) \( c \in a \cdot S \cap a \cdot T \) and (ii) \( c \in a \cdot S \cdot b \cdot T \).

(i) \( c \in a \cdot S \cap a \cdot T \). Hence,

\[ t_1[(a \cdot S \cdot a T) = t
\]

\[ (0)
\]

Now we derive
5.3. Internal Communications for a Blend

\[ t_0 \text{bat}_0 \in \text{t}(S \ w \ T) \land \ \text{tubt} \in \text{t}(S \ b \ T) \]

\[ \Rightarrow \ (t_0 = t_1c, \ t(S \ w \ T) \text{ is } \text{prefix-closed}) \]

\[ t_0 \text{bat}_1c \in \text{t}(S \ w \ T) \land t_0 \text{bat}_1 \in \text{t}(S \ w \ T) \land \text{tubt} \in \text{t}(S \ b \ T) \]

\[ \Rightarrow \ (\text{induction hypothesis, using } (0)) \]

\[ t_0 \text{bat}_1c \in \text{t}(S \ w \ T) \land t_0 \text{bat}_1 \in \text{t}(S \ w \ T) \]

\[ \Rightarrow \ (\text{Lemma 5.11}) \]

\[ t_0 \text{bat}_1c \in \text{t}(S \ w \ T) \]

\[ = (t_0 = t_1c) \]

\[ t_0 \text{bat}_0 \in \text{t}(S \ w \ T) \]

(ii) \( c \in \text{a}(S \ b \ T) \). Since \( t_0[(aS+aT)] = t \) we may assume trace \( t_2 \) to be such that

\[ t = t_2c \] (1)

and hence, since \( t_0 = t_1c \)

\[ t_1[(aS+aT)] = t_2 \] (2)

Now we derive

\[ t_0 \text{bat}_0 \in \text{t}(S \ w \ T) \land \text{tubt} \in \text{t}(S \ b \ T) \]

\[ \Rightarrow \ (t_0 = t_1c, (1), \text{and } t(S \ w \ T) \text{ and } t(S \ b \ T) \text{ are } \text{prefix-closed}) \]

\[ t_0 \text{bat}_1c \in \text{t}(S \ w \ T) \land t_0 \text{bat}_1 \in \text{t}(S \ w \ T) \land \text{tubt} \in \text{t}(S \ b \ T) \land \text{tubt} \in \text{t}(S \ b \ T) \]

\[ \Rightarrow \ (\text{induction hypothesis, using } (2)) \]

\[ t_0 \text{bat}_1c \in \text{t}(S \ w \ T) \land t_0 \text{bat}_1 \in \text{t}(S \ w \ T) \land \text{tubt} \in \text{t}(S \ b \ T) \]

\[ = (t_0[(aS+aT)] = s \text{ and } t_1[(aS+aT)] = t_2 \text{ according to } (2), \text{Distribution of projection over concatenation, using } a \in aS+aT, b \in aS+aT, \text{and } c \in aS+aT) \]

\[ t_0 \text{bat}_1c \in \text{t}(S \ w \ T) \land t_0 \text{bat}_1 \in \text{t}(S \ w \ T) \land \text{tubt} \in \text{t}(S \ b \ T) \]

\[ \Rightarrow \ (\text{Lemma 3.5, using the definition of blending, if } c \in \text{a}(S \ b \ T). \text{ Lemma 5.10, using } a \in \text{a}(S \ b \ T) \text{ and } b \in \alpha(S \ b \ T), \text{if } c \in \alpha(S \ b \ T)) \]

\[ t_0 \text{bat}_0 \in \text{t}(S \ w \ T) \]

\[ = (t_0 = t_1c) \]

\[ t_0 \text{bat}_0 \in \text{t}(S \ w \ T) \]

(End of Proof)
Lemma 5.18: For connectable \( C_4 \)'s \( S \) and \( T \), for traces \( s \) and \( t \), and for symbols \( a \in \iota(S \cdot b \cdot T) \) and \( b \in \alpha(S \cdot b \cdot T) \)

\[ sa \in t(S \cdot b \cdot T) \land sbt \in t(S \cdot b \cdot T) \rightarrow \]

\[ \exists s_0, s_1 : s_0 \in t(S \cdot w \cdot T) \land s_0b s_1 \in t(S \cdot w \cdot T) \land \]

\[ s_0 \in \alpha(S \cdot a \cdot T) = s \land s_1 \in \alpha(S \cdot a \cdot T) = t \]

Proof:

\[ sa \in t(S \cdot b \cdot T) \land sbt \in t(S \cdot b \cdot T) \]

\[ = \{ \text{definition of blending, } t(S \cdot w \cdot T) \text{ is prefix-closed} \} \]

\[ \exists s_0, s_1 : sa \in t(S \cdot b \cdot T) \land s_0 \in t(S \cdot w \cdot T) \land s_0b s_1 \in t(S \cdot w \cdot T) \land \]

\[ s_0 \in \alpha(S \cdot a \cdot T) = s \land s_1 \in \alpha(S \cdot a \cdot T) = t \]

\[ = \{ \text{Lemma 3.5, since } a \in \iota(S \cdot b \cdot T), \text{ using the definition of blending} \} \]

\[ \exists s_0, s_1 : sa \in t(S \cdot w \cdot T) \land s_0b s_1 \in t(S \cdot w \cdot T) \land \]

\[ s_0 \in \alpha(S \cdot a \cdot T) = s \land s_1 \in \alpha(S \cdot a \cdot T) = t \]

(End of Proof)

Example 5.0

Consider trace structure \( S \) with input alphabet \( \{ x, y \} \), output alphabet \( \{ a, b \} \), and command \( x \cdot a \cdot y \); \( b \) and consider trace structure \( T \) with output alphabet \( \{ x, y \} \), input alphabet \( \{ a, b \} \), and command \( x \cdot y \). Then \( S \) is, according to the rules, a \( C_2 \) and \( T \) a \( C_3 \). Alphabet \( \{ x, y \} \) is independent with respect to both trace structures. Moreover, \( a \cdot S \cap a \cdot T = (o \cdot S \cap o \cdot T) \cup (o \cdot T \cap o \cdot S) \) and \( S \cdot \{ x, y \} = T \cdot \{ x, y \} \), as a consequence of which \( S \) and \( T \) are connectable.

The trace set of \( S \cdot w \cdot T \) is \( \{ e, x, y, xa, yb \} \) and the trace set of \( S \cdot b \cdot T \) equals \( \{ e, a, b \} \). Neither \( xb \) nor \( xa \) is an element of \( t(S \cdot w \cdot T) \). Therefore, taking for \( x, t, y, a, \) and \( b \) in Lemma 5.16 \( e, e, e, a, \) and \( b \) respectively, there do not exist traces \( s_0, s_1, \) and \( s_2 \) with the properties as in Lemma 5.16. Consequently, Lemma 5.16 does not hold when replacing \( C_2 \) by \( C_3 \) (or \( C_4 \)).

(End of Example)
5.4. The closure of C₁

The blend of two connectable C₁'s satisfies R₀ through R₃, as has been proved in Section 5.2. What remains are the proofs for R₄' and R₅'. We prove R₄' for the composite of C₂', which is sufficient since C₁ ⊂ C₂.

Lemma 5.19: For connectable C₂'s S and T, for traces s and t, and for symbols a ∈ a(S b T) and b ∈ a(S b T) of different types

\[ sa \in t(S b T) \land sb \in t(S b T) \Rightarrow sab \in t(S b T) \]

Proof:

\[ sa \in t(S b T) \land sb \in t(S b T) \]

\[ \Rightarrow \{ \text{Lemma 5.16, symbols } a \text{ and } b \text{ are distinct since they are of different types} \} \]

\[ \exists s₀, x₁, s₂ : s₀b₀s₁ \in t(S w T) \land s₀b₁s₀ \in t(S w T) \land s₀ \left[ (a S + a T) = s \land x₁ \left[ (a S + a T) = e \land s₂ \left[ (a S + a T) = at \land a \neq b \right] \right] \right] \]

\[ \Rightarrow \{ t(S w T) \text{ is prefix-closed. Renaming and calculus, using } a \in a S + a T \} \]

\[ \exists s₀, x₁, s₂ : s₀b₀s₁ \in t(S w T) \land s₀b₁s₀ \in t(S w T) \land s₀ \left[ (a S + a T) = s \land x₁ \left[ (a S + a T) = e \land s₂ \left[ (a S + a T) = t \land b₁ \left[ (a S + a T) = e \right] \right] \right] \right] \]

\[ \Rightarrow \{ \text{if } a \in (S w T) \text{ we apply Lemma 5.0 followed by Lemma 5.9. If not, then } a \in a (S w T) \text{ and we apply Lemma 5.6} \} \]

\[ \exists s₀, x₁, s₂ : s₀b₀s₁s₂ \in t(S w T) \land s₀ \left[ (a S + a T) = s \land x₁ \left[ (a S + a T) = e \land s₂ \left[ (a S + a T) = t \right] \right] \right] \]

\[ \Rightarrow \{ \text{definition of blending, using } a \in a(S b T) \text{ and } b \in a(S b T) \} \]

\[ sab \in t(S b T) \]

(End of Proof)

Lemma 5.20: For connectable C₁'s S and T, for trace s, and for distinct symbols a ∈ a(S b T) and b ∈ a(S b T)

\[ sa \in t(S b T) \land sb \in t(S b T) \Rightarrow sab \in t(S b T) \]

Proof:

\[ sa \in t(S b T) \land sb \in t(S b T) \]

\[ \Rightarrow \{ \text{Lemma 5.16} \} \]
\[(\exists z_0, z_1, z_2 : \forall x, y \in \Sigma \cdot t(S w T) \land z_1 b_2 \in t(S w T) \land z_0 [(a S + a T) = x \land z_2 (a S + a T) = x)]\]

= \{ t(S w T) \text{ is prefix-closed } \}

\[(\exists z_2 : \forall b \in t(S w T) \land z_2 b \in t(S w T) \land z_0 [(a S + a T) = x)]\]

⇒ \{ Lemma 5.12 \}

\[(\exists z_0 : \forall a \in t(S w T) \land z_0 (a S + a T) = x)]\]

⇒ \{ definition of blending, using a \in a(S b T) \text{ and } b \in a(S b T) \}

\[sab \in t(S b T)\]

(End of Proof)

Now we have proved the following theorem.

**Theorem 5.0**: \( C_1 \) is closed under composition of connectable \( C_1 \)'s.

**Example 5.1**

Consider the C-wire element of Example 2.3 with input alphabet \( \{ a, q, r \} \) and output alphabet \( \{ b, p \} \) with command \( a ; (p ; (q ; b ; a), r) \) and the And-element as introduced in Example 2.6 with input alphabet \( \{ p, d \} \), output alphabet \( \{ c, q, r \} \), and command \( (p ; c ; d ; q, r ; p ; (c ; d ; r), q) \). They are both \( C_1 \)'s. Alphabet \( \{ p, q, r \} \) is independent with respect to both trace structures, as has been argued in Examples 3.1 and 3.3. Each common symbol is input in the one and output in the other trace structure, and projected on \( \{ p, q, r \} \) both trace structures yield the trace structure with command \( (p ; q, r) \). As a consequence, they are connectable and their composite is specified by the blend, being \( a ; (c ; d ; b ; a ; (b ; a), (c ; d)) \), which is a \( C_1 \), indeed. This element may be interpreted as a Quick Return Linkage (QRL) [10]. It has a cyclic way of operation. In the first half of the cycle a component informs another component via \( a \) of the presence of input data, and is notified via \( b \) that these data have been processed. The other component is notified of these data via \( c \) and informs the first component via \( d \) that these data have been processed. The second half of the cycle, the return-to-zero phase, then proceeds without any communications between both components.

(End of Example)

**Example 5.2**

Consider the C-element with two outputs of Example 2.2 with input alphabet
The closure of \( C_2 \)

\( \{ e, s \} \), output alphabet \( \{ d, t \} \), and command \( e, s ; ((d ; c), (t ; s)) \). Alphabets \( \{ e, d \} \) and \( \{ s, t \} \) are independent with respect to this element as has been argued in Example 3.0. This element may be composed with two QRL’s of the preceding example. The first QRL is exactly the one derived in that example. The other one is initialized in a different state and its symbols are renamed. Its input alphabet is \( \{ p, t \} \), its output alphabet \( \{ r, s \} \), and its command is \( (s ; (r ; p), (r ; t)) \). Alphabet \( \{ e, d \} \) is independent with respect to the first QRL, alphabet \( \{ s, t \} \) to the other QRL. The projections of the first QRL and of the C-element on \( (e, d) \) yield the trace structure with command \( (e, d) \). Since the input-in-the-one-and-output-in-the-other-one rule is obviously satisfied, these two components are connectable. Alphabet \( \{ s, t \} \) turns out to be independent with respect to the composite and the projections of the composite and the other QRL on \( (s, t) \) yields the trace structure with command \( (s, t) \). That makes these two components connectable as well. The result of their blending is \( a ; ((b ; a ; b ; a), (r ; p ; r ; p)) \). This may be interpreted as a binary semaphore [1]. Such a semaphore may be composed with another one, using \( \{ p, r \} \) for the one and \( \{ a, b \} \) for the other one as independent alphabet by means of which they are connected. The result is a ternary semaphore. In this way we can compose \( k - 1 \) binary semaphores, which yields a \( k \)-ary semaphore.

(End of Example)

5.5. The closure of \( C_2 \)

According to Section 5.2 and Lemma 5.19 the only rule left to prove is \( R_5 \).

Lemma 5.21: For connectable \( C_2 \)'s \( S \) and \( T \), for trace \( s \), and for distinct symbols \( a \in s(S b T) \) and \( b \in s(S b T) \), not both input symbols,

\[
\text{sa} \in t(S b T) \land \text{sb} \in t(S b T) \Rightarrow \text{sab} \in t(S b T)
\]

Proof:

\[
\begin{align*}
\text{sa} \in t(S b T) \land \text{sb} \in t(S b T) & \Rightarrow \text{Lemma 5.16} \\
(\exists \alpha, \beta, \gamma : \text{sa} \alpha \text{sb} \gamma \in t(S w T) \land \text{sab} \in t(S w T) \land \text{sab} \in (a S + a T) = s \\
\land s_1 [(a S + a T) = s] \land s_2 [(a S + a T) = s] \\
& \Rightarrow (t(S w T) \text{ is prefix-closed}) \\
(\exists \kappa : \text{sa} \kappa \in t(S w T) \land \text{sb} \kappa \in t(S w T) \land \text{sb} \kappa \in (a S + a T) = s) \\
& \Rightarrow (\text{Lemma 5.13}) \\
(\exists \kappa : \text{sb} \kappa \in t(S w T) \land \text{sb} \kappa \in (a S + a T) = s)
\end{align*}
\]
\[ \Rightarrow \{ \text{definition of blending, using } a \in a(3 \ b \ T) \text{ and } b \in a(3 \ b \ T) \} \]
\[ sab \in t(3 \ b \ T) \]

(End of Proof)

This means that we have proved the following theorem.

**Theorem 5.1:** \( C_3 \) is closed under composition of connectable \( C_3 \)'s.

**Example 5.3**
Consider the Three-wire component of Example 2.4 with input alphabet \( \{ x_0, x_1, b \} \), output alphabet \( \{ y_0, y_1, a \} \), and command \( (x_0, y_0; b; a | x_1; y_1; b; a)^* \). We can 'lengthen' these wires by composing this element with, apart from renaming, the same element. The latter is the component with input alphabet \( \{ y_0, y_1, c \} \), output alphabet \( \{ x_0, x_1, b \} \), and command \( (y_0; z_0; c; b | y_1; z_1; c; b)^* \). Alphabet \( \{ y_0, y_1, b \} \) is independent with respect to both components as has been argued in Example 3.2, and the projections on \( \{ y_0, y_1, b \} \) yields for both trace structures the trace structure with command \( (y_0; y_1; b)^* \). The blend of the two, being the specification of the composite, is \( (x_0; x_0; c; a | x_1; z_1; c; a)^* \). This is, apart from renaming, the same component as the ones that we started from.

(End of Example)

**Example 5.4**
Consider the buffer as introduced in Example 2.8 with input alphabet \( \{ x_0, x_1, b \} \), output alphabet \( \{ y_0, y_1, a \} \), and state graph
5.6. The closure of $C_4$

Left to prove for the blend of two $C_4$'s are $R'_T$ and $R''_T$.

Lemma 5.22 : For connectable $C_4$'s $S$ and $T$, for traces $s$ and $t$, and for symbols $a \in a(S \ b \ T)$, $b \in a(S \ b \ T)$, and $\epsilon \in a(S \ b \ T)$ such that $b$ is of another type than $a$ and $\epsilon$

\[ sabc \in t(S \ b \ T) \wedge sbat \in t(S \ b \ T) \Rightarrow sabt \in t(S \ b \ T) \]

Proof : We distinguish two cases : (i) $\epsilon \in \iota(S \ b \ T)$ and (ii) $\epsilon \in \alpha(S \ b \ T)$

(i) $\epsilon \in \iota(S \ b \ T)$. Now we derive

\[ sabc \in t(S \ b \ T) \wedge sbat \in t(S \ b \ T) \]

(definition of blending, using $a \in a(S \ b \ T)$ and $b \in a(S \ b \ T)$)

\[ (3s_0, s_1, t_0 : \text{ sabc } \in t(S \ b \ T) \wedge s_bbs_0 \in t(S \ w \ T) \wedge s_o[(aS \ a T) = s] \wedge t_0[(aS \ a T) = t] \]

\[ \Rightarrow \text{ Lemma 5.0, since the type of $a$, being the type of $\epsilon$, is input. Moreover,} \]

\[ t(S \ b \ T) \text{ is prefix-closed and remaining } \}

...(3s_0, t_0 : \text{ sabc } \in t(S \ b \ T) \wedge s_bbs_0 \in t(S \ w \ T) \wedge \]

\[ s_o[(aS \ a T) = s] \wedge t_0[(aS \ a T) = t] \]

\[ \Rightarrow \text{ Lemma 5.17, since $a$ is input and $b$ is of another type than $a$ } \}

...(3s_0, t_0 : \text{ sabc } \in t(S \ b \ T) \wedge s_bbs_0 \in t(S \ w \ T) \wedge \]
\[ s_0[(a S \mp a T) = s \land t_0[(a S \mp a T) = t] \]

\[ (\exists s_0, t_0 : s_0, t_0 \in t(S w T) \land s_0, t_0 \in t(S b T) \land s_0 t_0 = t_0 s_0 \land s_0 t_0 = t_0 s_0 \land t_0 s_0 = s_0 t_0 \land t_0 s_0 = t_0 s_0) \]

\[ \Rightarrow \{ \text{Lemma 3.5, using } c \in t(S b T) \text{ and the definition of blending} \} \]

\[ (\exists s_0, t_0 : s_0, t_0 \in t(S w T) \land s_0, t_0 \in t(S b T) \land s_0 t_0 = t_0 s_0 \land t_0 s_0 = t_0 s_0) \]

\[ \Rightarrow \{ \text{definition of blending, using } a \in a(S b T), b \in a(S b T), \text{and } c \in a(S b T) \} \]

\[ s_0 t_0 \in t(S b T) \]

(ii) \[ c \in a(S b T). \text{ In this case we derive} \]

\[ s_0 t_0 \in t(S b T) \land s_0 t_0 \in t(S b T) \]

\[ \Rightarrow \{ \text{definition of blending} \} \]

\[ (\exists s_0, t_0 : s_0, t_0 \in t(S w T) \land s_0, t_0 \in t(S b T) \land s_0 t_0 = t_0 s_0 \land s_0 t_0 = t_0 s_0 \land t_0 s_0 = s_0 t_0 \land t_0 s_0 = t_0 s_0) \]

\[ \Rightarrow \{ \text{Lemma 5.1, since the type of } a, \text{ being the type of } c, \text{ is output. Moreover,} \}

\[ t_0 s_0 \in t(S b T) \text{ is prefix-closed and renaming} \} \]

\[ (\exists s_0, t_0 : s_0, t_0 \in t(S w T) \land s_0, t_0 \in t(S b T) \land s_0 t_0 = t_0 s_0 \land t_0 s_0 = t_0 s_0) \]

\[ \Rightarrow \{ \text{Lemma 5.10, since } a \text{ is output and } b \text{ of another type than } a \} \]

\[ (\exists s_0, t_0 : s_0, t_0 \in t(S w T) \land s_0, t_0 \in t(S b T) \land t_0 s_0 = t_0 s_0 \land t_0 s_0 = t_0 s_0) \]

\[ \Rightarrow \{ \text{definition of blending, using } a \in a(S b T), b \in a(S b T), \text{and } c \in a(S b T) \} \]

\[ s_0 t_0 \in t(S b T) \]

(End of Proof)
Lemma 5.23: For connectable $C_4$'s $S$ and $T$, for trace $s$, and for symbols $a \in a(S \cdot b T)$ and $b \in a(S \cdot b T)$ of different types

\[
sa \in t(S \cdot b T) \land sb \in t(S \cdot b T) \Rightarrow sab \in t(S \cdot b T)
\]

Proof: We assume $a \in t(S \cdot b T)$ and, hence, have to prove

\[
sa \in t(S \cdot b T) \land sb \in t(S \cdot b T) \Rightarrow sab \in t(S \cdot b T) \land s \in t(S \cdot b T)
\]

\[
= \{ \text{Lemma 5.18, using that } t(S \cdot w T) \text{ is prefix-closed} \}
\]

\[
(s_0 : sa \in t(S \cdot w T) \land s_0 b \in t(S \cdot w T) \land s_0 [(a \cdot S \cdot a \cdot T) = s])
\]

\[
\Rightarrow \{ \text{Lemma 5.14} \}
\]

\[
(s_0 : sab \in t(S \cdot w T) \land s \in t(S \cdot w T) \land s_0 [(a \cdot S \cdot a \cdot T) = s])
\]

\[
\Rightarrow \{ \text{definition of blending, using } a \in a(S \cdot b T) \text{ and } b \in a(S \cdot b T) \}
\]

\[
sab \in t(S \cdot b T) \land s \in t(S \cdot b T)
\]

(End of Proof)

This completes the proof of

Theorem 5.2: $C_4$ is closed under composition of connectable $C_4$'s.

Example 5.5

Consider the $C_3$ of Example 2.12 with input alphabet $\{ a, d, e \}$, output alphabet $\{ b, e, f \}$, and command $(((f ; a) ; (b ; d))^* ; f ; a ; (e ; e ; b ; d)^* ; b ; d)^*$. As argued in Example 3.6, alphabet $\{ e, f \}$ is independent. Projection on $\{ e, f \}$ yields the trace structure with command $((e ; e)^*)$. The trace structure with command $((e ; e)^*)$, input alphabet $\{ e \}$, and output alphabet $\{ e \}$ is a $C_1$. These two components are connectable. Composition of the two yields the projection of the first trace structure onto $\{ a, b, d, f \}$, which is, according to Example 3.6, not a $C_2$.

(End of Example)
6

Suggestions for further study

The theory developed in this monograph provides a base for a theory on delay-insensitive circuits. In this chapter we point out a number of generalizations that might be considered.

In Chapter 3 we noticed already that the requirements for connectability of trace structures \( S \) and \( T \) are rather restrictive. The first relaxation considered relates to the requirement that their projections on the set of common symbols be the same. This is, provided that the set of common symbols is independent with respect to both trace structures, a sufficient condition to guarantee absence of computation interference. There is nothing wrong, however, with a situation in which the one component is able to receive an input that the other component is never able to produce as output. An example of this kind is a variable, as we have introduced in Example 2.7, that is composed with another component that always retrieves a stored value twice before storing a next value in the variable. It might be sufficient to require

\[
(\forall a : \alpha \in S \cap T : \exists \sigma \in tS[(aS \cap aT) \Rightarrow \sigma \in tT[(aS \cap aT)]) \land \\
(\forall a : \alpha \in T \cap iS : \exists \sigma \in tT[(aS \cap aT) \Rightarrow \sigma \in tS[(aS \cap aT)])
\]

for all traces \( \tau \in tS[(aS \cap aT) \cap tT[(aS \cap aT)]. \) When taking delays into account it is not obvious that this requirement is sufficient to guarantee absence of computation interference. This should be proved again by means of composability of traces as we did in Chapter 4 for the more restrictive composition operator. Expressing requirements in terms of individual traces is undesirable, however. We would like to express this or a more suitable requirement in terms of trace structure. How this should be done remains to be seen.

Connecting trace structures by means of independent alphabets seems too restrictive a requirement as well. Connecting two wires with one another in the usual way, or connecting a C-element with a Fork to obtain a C-element with
two outputs is still impossible. We would like to be able to compose trace structures with a set of common symbols that is not independent with respect to each of them. How to incorporate this kind of composition is not clear yet. The following might be a possible strategy.

Consider two components that are specified by trace structures $S$ and $T$ respectively. Let $C$ be the set of common symbols and let $C$ be independent with respect to neither $S$ nor $T$. For trace structure $S$ this means that there is a trace $t \in tS$ and a symbol $a \in C \cap tS$ such that $(t \mid C)a \in tS \mid C$ and $ta \notin tS$ (or something similar with $C$ replaced by $aS \setminus C$). In other words, there are still communications to be performed by means of the symbols of the complement with respect to $aS$ before input $a$ can be received by the component. The two environments that the environment of $S$ is partitioned into by $C$ and $aS \setminus C$ cannot communicate with the component independent of one another. They need additional information on each other's progress. Therefore we could introduce an alphabet $D$ of fresh symbols via which the two environments can directly communicate. In order to reflect these communications traces of $S$ should be interspersed with symbols of $D$ in a suitable way. The component-environment pair now becomes a triple:

Having done something similar with trace structure $T$, using the same set of symbols $D$, we can blend the new $S$ and $T$, provided that $S \mid (C \cup D)$ and $T \mid (C \cup D)$ meet certain conditions, e.g. $S \mid (C \cup D) = T \mid (C \cup D)$.

The problem of course is the interspersing of traces of $S$ with symbols of $D$. One of the questions is what requirements to impose upon the resulting trace structure. A necessary, and possibly sufficient, condition seems to be that the projections of the new trace structure onto $C \cup D$ and $(aS \setminus C) \cup D$ be delay-insensitive. A second question is how to find $D$ and how to construct the desired trace structure. A trivial way is to conceive one of the environments, $E_0$ say, as a pass-through for all incoming signals. This means that there is a one-to-one correspondence between input symbols of $C$ and output symbols of $D$ and between output symbols of $C$ and input symbols of $D$. (Input and output is
here with respect to \( E_0 \). Moreover, in the specification of the communications via \( C \) and \( D \) every input is followed by its corresponding output and so repeatedly. The specification of the communications via \( D \) and a \( C \) \( \setminus C \) is in this case the same as the one via \( C \) and a \( C \) \( \setminus C \) with every symbol of \( C \) replaced by its corresponding symbol in \( D \).

Once we have properly relaxed the requirements for connectability and have proved the absence of transmission and computation interference we have to answer the question whether and how to incorporate multiple transitions on a wire in our formalism. As we have pointed out in the introduction, we can allow multiple transitions on a wire in the presence of a data valid wire that signals the validity of a voltage level on the first wire. This kind of protocol is often used for data transmission. A high level on a wire represents a logical one and a low level on that wire a logical zero. Having \( n \) data wires we can convey \( 2^n \) different values.

Not using a data valid wire we encode data by a so-called \( m \) out of \( n \) coding. Having \( n \) wires a transition on exactly \( m \) of them, \( 0 \leq m \leq n \), represents a value. In this way we can convey \( \binom{n}{m} \) different values. For fixed \( n \) the maximum value of \( \binom{n}{m} \) is asymptotically \( 2^{m/\sqrt{n}} \). Notice that we have used a 1 out of 2 coding for the data transmission in the examples.

The advantage of data transmission with a data valid wire is the smaller number of wires needed and the availability of circuits that can handle data encoded in this way, e.g. adders and multipliers. The number of wires used to convey data, however, is typically 8, which makes, together with the data valid wire, a total of 9 wires required. An \( m \) out of \( n \) coding requires 11 wires for 8 bits of information, which does not seem to be too large a difference. An interesting question is how to build arithmetical circuits that can handle data encoded in this way. It might just be that this encoding seems more difficult only because we are used to the other one.

A question, which is often posed, is whether there exists a (finite) base for delay-insensitive circuits, i.e. a (finite) set of delay-insensitive circuits by means of which we can obtain all delay-insensitive circuits by composition. Once we have relaxed the requirements for composition of components this is a valid question. It might be that there exists a base consisting of just a few elements, which would make a gate array approach for the implementation of a component as chip very attractive. Closure properties of classes may be helpful in finding such a set. Using, for instance, the composition operator as defined in Chapter 3 we cannot obtain a \( C_4 \) from \( C_2 \)'s. This means that an arbiterlike device necessarily belongs to a base. It is very likely that the closure properties derived in Chapter 3 hold for less restrictive composition operators as well.

Trace theory as it is used here provides the first step towards a high level specification language that we would like a silicon compiler, our ultimate goal, to be able to accept. Specifying circuits at the current level of abstraction is a nuisance. Another topic of research, therefore, is how to translate specifications
written at a higher level of abstraction, like for instance the specifications in [12], into specifications that satisfy the rules for delay-insensitivity and still have, in some sense, the same meaning. One can think, for example, of adding symbols to guarantee proper communications.
Concluding remarks

In this monograph we have discussed specifications of circuits when making no assumptions on wire delays. This has led to a definition and a classification of delay-insensitive circuits. Moreover, we have proposed a composition operator that we have shown to warrant internal communications that are free of transmission and computation interference. Three of the four classes turn out to be closed under this composition operator. A few final remarks on the results obtained seem to be appropriate.

C₁, C₂, and C₃ arose from an intuitive understanding of delay-insensitive circuits and of decisions that are to be made in the component and in the environment. Since C₃ turned out not to be closed under the composition operator proposed, the need for a larger, still physically interpretable, class developed. C₄ is a class that satisfies these requirements, which makes C₃, in fact, obsolete.

Petri nets [7] are frequently used for the specification of delay-insensitive circuits. They suffer, however, from a canonical form problem, i.e. distinct Petri nets may specify the same circuit. This makes it hard to capture properties of delay-insensitive circuits in terms of Petri nets. Trace structures do not suffer from this canonical form problem and are, therefore, more suited to define and classify delay-insensitive components. Petri nets can, like our program texts or state graphs, very well be used for the representation of trace structures. The question whether there is a representation that should be preferred to the others is not easily answered. Probably it depends on the circumstances under which they are to be used and on the question by whom they are to be used.

We have confined our attention to components that satisfy the rules for delay-insensitivity and we have defined for that class of components a composition operator. The advantage of this approach is that it is not necessary to take wire delays into account when composing components: the blend has been shown in Chapter 4, with some effort but we only have to do it once, to be a proper composition operator for this kind of components. This is opposed to the
approach taken in [12] where a larger class of components is considered. When composing this kind of components, however, one cannot simply use the blend but one needs a much more complicated composition operator, called agglutination. The result of such an agglutination is not easily computed. Confining the class of components to be considered seems to be a better approach for dealing with wire delays.
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Samenvatting

In dit proefschrift wordt een definitie en een classificatie van en een compositiemethode voor vertragingsongevoelige circuits besproken. Dit zijn circuits waarvoor geen aannamen gemaakt worden omtrent vertragingen in verbindingsdraden of omtrent de snelheid waarmee de input signalen reageert op input signalen. De reden voor de bestudering van dergelijke circuits is tweeërlei. Enerzijds bestaan er circuits die niet altijd binnen een bepaalde tijd een berekening hebben uitgevoerd. Dit betekent dat er in de specificatie van een circuit dat met zo een circuit wordt verbonden niet van uitgegaan mag worden dat input signalen binnen een zekere tijd na de output signalen zullen kunnen worden ontvangen. Anderzijds blijkt dat door het verkleinen van geïntegreerde schakelingen de vertragingstijden van elektrische signalen in verbindingsdraden kunnen worden vergroot met de schakelstijden van transistoren, zodat vertragingen in draden niet langer verwaarloosd mogen worden.

Een viertal klassen van vertragingsongevoelige circuits wordt op axiomatische wijze gedefinieerd. Drie van deze klassen worden de voor- en de vierde dit niet is. Voor de specificatie van circuits en voor de geslotenheidsconditions wordt gebruik gemaakt van trace theory. Dit is een theorie van symbolen en verzamelingen symbolen.

Bij het samenstellen van circuits dient aan twee voorwaarden te zijn voldaan. Ten eerste moet gegarandeerd zijn dat elektrische signalen op een verbinding draad niet met elkaar kunnen interfereren. Door geen aannamen te maken over vertragingen betekent dit dat hooguit één signaal per draad is toegestaan. Daarnaast mag een elektrisch signaal pas bij een circuit arriveren als dat circuit, volgens zijn specificatie, in staat is de ontvangst van dat signaal. Van de in dit proefschrift voorgestelde compositemethode wordt aangetoond dat bij compositie van vertragingsongevoelige circuits aan deze beide voorwaarden is voldaan.
Curriculum vitae

STELLINGEN

behorende bij het proefschrift

Classification and composition of delay-insensitive circuits

van

Jan Tijmen Udding

Eindhoven,
25 september 1984
0. Voor iedere natuurlijke $q$ en $m$ waarvoor geldt $m \neq 1$, $q \geq 2m + 1$, $q \equiv (1$ of $3 \mod 6)$ en $m \equiv (1$ of $3 \mod 6)$ bestaan er twee Steiner triple systems van orde $q$ (op dezelfde verzameling punten) die precies een Steiner triple system van orde $m$ gemeen hebben.


1. Voor gegeven alfabet $A$ en prefix-closed trace set $U$ heeft de vergelijking $T \subseteq A^+$: $T = U b_1 T$ precies één oplossing die $\epsilon$ bevat indien voor iedere $u \in U$ geldt $I(u \cdot A) \neq I(u \cdot A)$.

Lit: J.T. Udding, On recursively defined sets of traces, Intern Memorandum, JTU0a, 1983.

2. De klassen van vertragingsongevoelige trace structures die voldoen aan de regels $R_9$ tot en met $R_9''$ en aan ofwel $R_9'$ dan wel $R_9'''$ zijn gesloten onder compositie van connectable trace structures.

Lit: Dit proefschrift.

3. Het is opvallend en valt te betreuren dat in zo weinig boeken over tralietheorie aandacht wordt besteed aan eigenschappen van morfismen.

4. Het blijven uitbreiden van het relationele model draagt geenzins bij tot een goede fundering van de theorie over informatiesystemen.

5. Imperatieve programmeertalen zijn een erfenis uit de tijd dat het doel van een taal nog was het programmeren van een machine. Nu machines er zijn om onze programma’s uit te voeren, dient aanzienlijk meer aandacht te worden besteed aan het gebruik van non-imperatieve programmeertalen dan momenteel het geval is.

6. Bij het beschrijven van fysische objecten door middel van een wiskundig model dienen objecten die in het model van elkaar verschillen te corresponderen met objecten die in fysische redenen van elkaar verschillen. Petri netten dienen derhalve niet gebruikt te worden voor de specificatie van vertragingsongevoelige systemen.
7. Slechte ervaringen met inadequate formalismen hebben geleid tot een schromelijke onderschatting van de mogelijke rol van formalismen.

8. Het idee dat bewijsvoering gereduceerd kan worden tot formulemanipulatie getuigt van een schromelijke overschatting van de mogelijke rol van formalismen.


10. Gelukkig is, zoals de naam al zegt, temporele logica maar tijdelijk.

11. Binnenkort zal de zogenaamde ‘school met de computer’ zijn intrede doen in de strijd om de gunst van de leerplichtige. De suggestie als zou een dergelijke school een streepje voor hebben op andere scholen is onjuist en dient als misleiding te worden aangemerkt.